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(54) Title: MULTI-BAND TRANSCEIVER UTILIZING DIRECT CONVERSION RECEIVER AND DIRECT CONVERSION RE-CEIVER			
(57) Abstract A multi-band transceiver having a receiver portion and a transmitter portion, wherein the receiver portion includes a direct conversion receiver system for directly downconverting a signal to baseband frequencies. The direct conversion receiver system includes a frequency translator having first and second inputs and output. A first signal at a first frequency is applied to the first input. A second signal having a second frequency is applied to the second input. The first frequency is preferably an n th order subharmonic of the second frequency, wherein n is an integer greater than 1. A low pass filter is integral with or inherent to the first input, and a high pass filter is integral with or inherent to the second input. The corner frequencies of both the low pass and high pass filters is above the first frequency and below the second frequency. A direct conversion receiver system is also provided in which a first input signal at a first frequency is applied to a first input port of a multiplier, a second input signal at a second frequency equal to about $1/n$ times the first frequency, wherein n is an integer, is applied to a second input port of the multiplier. A first filter coupled to the first input port is configured to substantially filter out any leakage at the second frequency which may be present. A second filter coupled to the second input port is configured to substantially filter out any leakage at the first frequency which may be present. The multiplier is configured to produce a signal at an output port thereof which is derived from the product of the first and second signals. In one embodiment, the output is representative of the product of the filtered first signal and a multiplication factor which switches at n times the second frequency. The output of the multiplier is coupled to a third filter. The output signal has a baseband component and another component. The third filter is configured to substantially filter out the other component and substantially maintain the baseband component in the output signal. In one implementation example, the multiplier is a mixer initializing half-frequency injection, such that the LO frequency is about $1/2$ the RF frequency.			

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S P E C I F I C A T I O N

MULTI-BAND TRANSCEIVER UTILIZING DIRECT CONVERSION RECEIVER AND DIRECT CONVERSION RECEIVER

BACKGROUND OF THE INVENTION

I. Field of the Invention

This invention relates generally to the field of wireless communications, and, more specifically, to a multi-band transceiver and a direct conversion receiver for a wireless communication device or handset.

II. Background

Wireless communication systems are an integral component of the ongoing technology revolution. Mobile radio communication systems, such as cellular telephone systems, are evolving at an exponential rate. In a cellular system, a coverage area is divided into a plurality of "cells". A cell is the coverage area of a base station or transmitter. Low power transmitters are utilized, so that frequencies used in one cell can also be used in cells that are sufficiently distant to avoid interference. Hence, a cellular telephone user, whether mired in traffic gridlock or attending a meeting, can transmit and receive phone calls so long as the user is within a "cell" served by a base station.

Mobile cellular systems were originally developed as analog systems. After their introduction for commercial use in the early 1980s, mobile cellular systems began to experience rapid and uncoordinated growth. In Europe, for example, individual countries developed their own systems. Generally, the systems of individual countries were incompatible, which constricted mobile communications within national boundaries and restricted the market for mobile equipment developed for a particular country's system. In 1982, in order to address this growing problem, the Conference of European Posts and Telecommunications (CEPT) formed the Groupe Spéciale Mobile (GSM) to study and develop a set of common standards for a future pan-European cellular network. It was recommended

that two blocks of frequencies in the 900 MHz range be set aside for the system. The initial goals for the new system included international roaming ability, good subjective voice quality, compatibility with other systems such as the Integrated Services Digital Network (ISDN), spectral efficiency, low handset and base station costs, and the ability to support new services and a high volume of users.

5 One of the initial, major decisions in the development of the GSM standard was adoption of a digital, rather than an analog, system. As mentioned above, analog systems were experiencing rapid growth and the increasing demand was straining the capacity of the available frequency bands. Digital systems offer improved spectral efficiency and are more cost efficient. The quality of digital transmission is also superior to that of analog transmission. Background sounds
10 such as hissing and static and degrading effects such as fadeout and cross talk are largely eliminated in digital systems. Security features such as encryption are more easily implemented in a digital system. Compatibility with the ISDN is more easily achieved with a digital system. Finally, a digital approach permits the use of Very Large Scale Integration (VLSI), thereby facilitating the development of cheaper and
15 smaller mobile handsets.

In 1989, the European Telecommunications Standards Institute (ETSI) took over responsibility for the GSM standards. In 1990, phase I of the standard was published and the first commercial services employing the GSM standard were launched in 1991. It was also renamed in 1991 as the Global System for Mobile
20 Communications (still GSM). After its early introduction in Europe, the standard was elevated to a global stage in 1992 when introduced in Australia. Since then, GSM has become the most widely adopted and fastest growing digital cellular standard, and is positioned to become the world's dominant cellular standard. With (currently) 324 GSM networks in operation in 129 countries, GSM provides almost
25 complete global coverage. As of January 1999, according to the GSM

Memorandum of Understanding Association, GSM accounted for more than 120 million subscribers. Market research firms estimate that by 2001, there will be more than 250 million GSM subscribers worldwide. At that time, GSM will account for almost 60% of the global cellular subscriber base, with yearly shipments exceeding 100 million phones.

Two frequency bands of 25 MHz were allocated for GSM use. As illustrated in **Figure 1a**, the 890-915 MHz band is reserved for transmission or "uplink" (mobile station to base station), and the 935-960 MHz band is reserved for reception or "downlink" (base station to mobile station). An extra ten MHz of bandwidth was later added to each frequency band. The standard incorporating this extra bandwidth (two 35 MHz bands) is known as Extended GSM (EGSM). In EGSM, the transmission band covers 880-915 MHz and the receiving band covers 925-960 MHz (**Figure 1b**). The terms GSM and EGSM are used interchangeably, with GSM sometimes used in reference to the extended bandwidth portions (880-890 MHz and 925-935 MHz). Sometimes, the originally specified 890-915 MHz and 935-960 MHz bands are designated Primary GSM (PGSM). In the following description, GSM will be used in reference to the extended bandwidth (35 MHz) standard.

Due to the expected widespread use of GSM, capacity problems in the 900 MHz frequency bands were anticipated and addressed. ETSI had already defined an 1800 MHz variant (DCS or GSM 1800) in the first release of the GSM standard in 1989. In DCS, the transmission band covers 1710-1785 MHz and the receiving band covers 1805-1880 MHz (**Figure 1c**). In the United States, the Federal Communications Commission (FCC) auctioned large blocks of spectrum in the 1900 MHz band, aiming to introduce digital wireless networks to the country in the form of a mass market Personal Communication Service (PCS). The GSM service in the US is known as PCS or GSM 1900. In PCS, the transmission band covers 1850-1910 MHz and the receiving band covers 1930-1990 MHz (**Figure 1d**).

Regardless of which GSM standard is used, once a mobile station is assigned a channel, a fixed frequency relation is maintained between the transmit and receive frequency bands. In GSM (900 MHz), this fixed frequency relation is 45 MHz. If, for example, a mobile station is assigned a transmit channel at 895.2 MHz, its receive channel will always be at 940.2 MHz. This also holds true for DCS and PCS; the frequency relation is just different. In DCS, the receive channel is always
5 95 MHz higher than the transmit channel and, in PCS, the receive channel is 80 MHz higher than the transmit channel. This frequency differential will be referred to in the ensuing discussion as the frequency offset.

The architecture of one implementation of a GSM network 20 is depicted in block form in **Figure 2**. GSM network 20 is divided into four interconnected
10 components or subsystems: a Mobile Station (MS) 30, a Base Station Subsystem (BSS) 40, a Network Switching Subsystem (NSS) 50 and an Operation Support Subsystem (OSS) 60. Generally, MS 30 is the mobile equipment or phone carried by the user; BSS 40 interfaces with multiple MSs 30 and manages the radio transmission paths between the MSs and NSS 50; NSS 50 manages system
15 switching functions and facilitates communications with other networks such as the PSTN and the ISDN; and OSS 60 facilitates operation and maintenance of the GSM network.

Mobile Station 30 comprises Mobile Equipment (ME) 32 and Subscriber Identity Module (SIM) 34. ME 32 is typically a digital mobile phone or handset.
20 SIM 34 is a memory device that stores subscriber and handset identification information. It is implemented as a smart card or as a plug-in module and activates service from any GSM phone. Among the information stored on SIM 34 are a unique International Mobile Subscriber Identity (IMSI) that identifies the subscriber to system 20, and an International Mobile Equipment Identity (IMEI) that uniquely
25 identifies the mobile equipment. A user can access the GSM network via any GSM

handset or terminal through use of the SIM. Other information, such as a personal identification number (PIN) and billing information, may be stored on SIM 34.

MS 30 communicates with BSS 40 across a standardized "Um" or radio air interface 36. BSS 40 comprises multiple base transceiver stations (BTS) 42 and base station controllers (BSC) 44. A BTS is usually in the center of a cell and consists of one or more radio transceivers with an antenna. It establishes radio links and handles radio communications over the Um interface with mobile stations within the cell. The transmitting power of the BTS defines the size of the cell. Each BSC 44 manages multiple, as many as hundreds of, BTSs 42. BTS-BSC communication is over a standardized "Abis" interface 46, which is specified by GSM to be standardized for all manufacturers. The BSC allocates and manages radio channels and controls handovers of calls between its BTSs.

The BSCs of BSS 40 communicate with network subsystem 50 over a GSM standardized "A" interface 51. The A interface uses an SS7 protocol and allows use of base stations and switching equipment made by different manufacturers. Mobile Switching Center (MSC) 52 is the primary component of NSS 50. MSC 52 manages communications between mobile subscribers and between mobile subscribers and public networks 70. Examples of public networks 70 that MSC 52 may interface with include Integrated Services Digital Network (ISDN) 72, Public Switched Telephone Network (PSTN) 74, Public Land Mobile Network (PLMN) 76 and Packet Switched Public Data Network (PSPDN) 78.

MSC 52 interfaces with four databases to manage communication and switching functions. Home Location Register (HLR) 54 contains details on each subscriber residing within the area served by the MSC, including subscriber identities, services to which they have access, and their current location within the network. Visitor Location Register (VLR) 56 temporarily stores data about roaming subscribers within a coverage area of a particular MSC. Equipment Identity

Register (EIR) 58 contains a list of mobile equipment, each of which is identified by an IMEI, which is valid and authorized to use the network. Equipment that has been reported as lost or stolen is stored on a separate list of invalid equipment that allows identification of subscribers attempting to use such equipment. The Authorization Center (AuC) 59 stores authentication and encryption data and parameters that verify a subscriber's identity.

5 OSS 60 contains one or several Operation Maintenance Centers (OMC) that monitor and maintain the performance of all components of the GSM network. OSS 60 maintains all hardware and network operations, manages charging and billing operations and manages all mobile equipment within the system.

10 The GSM transmitting and receiving bands are divided into 200 kHz carrier frequency bands. Using Time Division Multiple Access techniques (TDMA), each of the carrier frequencies is subdivided in time into eight time slots. Each time slot has a duration of approximately 0.577 ms, and eight time slots form a TDMA "frame", having a duration of 4.615 ms. One implementation of a conventional TDMA frame 80 having eight time slots 0-7 is illustrated in **Figure 3**.

15 In this conventional TDMA framework, each mobile station is assigned one time slot for receiving data and one time slot for transmitting data. In TDMA frame 80, for example, time slot zero has been assigned to receive data and time slot four has been assigned to transmit data. The receive slot is also referred to as the downlink slot and the transmit slot is referred to as the uplink slot. After the eight
20 slots, the remaining slots are used for offset, control, monitoring and other operations. This framework permits concurrent reception by as many as eight mobile stations on one frequency and concurrent transmission by as many as eight mobile stations on one frequency.

25 As described above, there are currently three GSM frequency bands defined. With the proliferation of wireless handset usage showing no signs of slowing down,

it is likely that additional bands will be defined in the future. Hence, GSM mobile stations intended for global usage should have multi-band capability.

Unfortunately, because of the widely disparate frequency ranges of the GSM, DCS, and PCS systems, a transceiver with a single main oscillator has not been able to cover the required frequencies. Moreover, designs employing separate oscillators for each of the bands are not feasible because of the cost involved, while designs
5 employing a single switchable oscillator typically suffer from poor performance.

Another problem is that current multi-band handsets utilize off-chip components such as the receiver's intermediate frequency (IF) filter which, in one conventional design, comprises a surface acoustical wave (SAW) filter. Components such as this tend to be large and bulky, and consume excessive space.
10 Thus, they are inconsistent with subscribers' demand for handsets are as compact, lightweight, and mobile as possible.

This problem can be further explained with reference to **Figure 20**, which shows a conventional receiver design. As shown, the receiver comprises a first mixer 4001, having a radio frequency (RF) input port 4005 coupled to an antenna
15 4011, and a local oscillator (LO) input port 4008. The mixer has an output port 4012 coupled, through signal line 4006, to the input of a band-pass filter (BPF) 4003. The BPF 4003 has an output coupled to the intermediate frequency (IF) input port 4010 of a second mixer 4002. The second mixer 4002 also has a local oscillator (LO) input port 4009. The output port 4014 of mixer 4002 is coupled, through
20 signal line 4015, to the input of low-pass filter (LPF) 4004. The output of LPF 4004 is coupled to signal line 4007.

First mixer 4001 is configured to multiply the signals received at the RF and LO input ports thereof, and provide the multiplied signal at the output port thereof. The frequency of the signal received at the RF input port is f_{RF} , and the frequency of
25 the signal received at the LO input port is f_{LO1} . The signal received at the RF input

port is derived from a signal received over antenna 4011. Typically, this signal represents a digitized audio signal which has been modulated onto an RF carrier signal. In the following discussion, this digitized audio signal will be referred to as the baseband signal, but it should be appreciated that, in practice, the baseband signal can be a desired signal other than a digitized audio signal, including a data signal.

5 The signal provided at the output port 4012 will have first order components at frequencies $f_{RF}-f_{LO1}$ and $f_{RF}+f_{LO1}$. The frequency $f_{RF}-f_{LO1}$ is an intermediate frequency which will be referred to as f_{IF} . In one implementation, f_{RF} is 900 MHz, f_{LO1} is 450MHz, and f_{IF} is 450 MHz. In this implementation, the first order components of the output signal will be at 1350 MHz and 450 MHz.

10 BPF 4003 has a passband centered at f_{IF} , and is configured to allow passage of the IF component of the output signal, and to prevent passage of the other first order component, that is, the component at the frequency $f_{RF}+f_{LO1}$. BPF 4003 also rejects any unwanted signals outside the desired band around $f_{RF}-f_{LO1}$. This IF component is then provided as an input to input port 4010 of mixer 4002.

15 The signal provided as an input to the LO port of mixer 4002 has a frequency f_{LO2} . This frequency is selected so that it is the same as the frequency f_{IF} of the signal provided at the input port 4010. Mixer 4002 multiplies these two signals, and provides the multiplied signal on output port 4014. The output signal will have two first order components, one at the baseband frequency, f_{BB} , and the other at twice the
20 intermediate frequency f_{IF} .

 The output signal from mixer 4002 is provided as an input to LPF 4004. LPF 4004 is configured to allow passage of the baseband component of the signal output from mixer 4002, and prevent passage of the high frequency component, that is, the frequency at twice f_{IF} , of the output of mixer 4002. The baseband component
25 is thus provided as an output of the receiver line 4007.

In operation, the receiver of **Figure 20** functions as follows. A signal is received over antenna 4011 representing a baseband signal modulated onto an RF carrier signal. The signal is passed through mixer 4001, which produces at its output port a signal having a first order component representative of the baseband signal, but at an intermediate frequency rather than baseband frequencies, and also a second first order component. The signal is passed through BPF 4002 to isolate the
5 intermediate frequency component from the other first order component. This intermediate frequency component is then passed through mixer 4002, which produces at its output port a signal having a baseband component and an intermediate frequency component. The signal is then passed through LPF 4004 to isolate the baseband component from the higher frequency component. LPF 4004
10 thus produces at its output a signal representative of the baseband signal.

As is apparent from the foregoing description, the operation of the receiver of **Figure 20** proceeds in two basic steps. In the first step, the baseband portion of the incoming RF signal is down converted to an intermediate frequency. In the second step, the baseband portion at the intermediate frequency is down converted to
15 the baseband frequency. Each of these steps is performed on and through distinct elements, the first step, through mixer 4001 and BPF 4003, and the second step, through mixer 4002 and LPF 4004.

Because of the cost and complexity of downconverting the baseband portion in multiple steps in the receiver of **Figure 20**, and the cost of the elements needed to
20 perform these multiple steps, the receiver of **Figure 20** is not ideal.

Direct conversion receivers eliminate the need for IF filters. However, current direct conversion receivers are susceptible to self-conversion to DC of the local oscillator signal or large RF blockers.

This problem can be further explained with reference to **Figure 4**, which
25 illustrates a conventional direct conversion receiver. As illustrated, the receiver of

Figure 4 comprises an antenna 200 coupled to the radio frequency ("RF") input port 219 of mixer 211. Mixer 211 has a local oscillator ("LO") input port 214, and an output port 201. The mixer mixes the signals provided at the RF and LO input ports, and provides the mixed signal to the output port. In the receiver of **Figure 4**, the frequency of the signal provided at the LO input port, f_{LO} , is matched to the frequency of the carrier frequency, f_{RF} , of the signal provided at the RF input port such that $f_{LO}=f_{RF}$. The mixed signal provided at the output port 201 of mixer 211 has a first order component at the baseband frequency, f_{BB} , and a first component at twice the LO or RF carrier frequencies, or $2f_{LO}$.

The output port 201 of mixer 214 is coupled to LPF 212 through signal line 213. The purpose of LPF 212 is to select only the baseband component of the signal output from mixer 211 while suppressing the higher frequency component at the frequency $2f_{LO}$. LPF 212 also rejects any unwanted signals outside the desired band around f_{BB} . The output of the LPF 212 is provided on signal line 215. It represents the baseband portion of the RF signal received over antenna 200.

An advantage of the design of **Figure 4** is the elimination of the IF filter, and related components such as a second mixer. However, a problem with this design is its vulnerability to leakage between the signals on the RF and IF input ports of the mixer. This problem is explained further in the following section.

With reference to **Figure 4**, consider the case in which a portion of the signal provided at the LO input port leaks onto the RF input port. Such is identified with reference numeral 216 in **Figure 4**. This portion will be mixed by mixer 211 with the original LO signal, thus producing a distortion in the output signal at the baseband frequency. Since this distortion is at the baseband frequency, it will pass through LPF 212, and appear in the output signal provided on signal line 215. The result is that this output signal is distorted in relation to the original transmitted baseband signal.

Consider next the case in which a portion of the signal provided at the RF input port leaks onto the LO input port. Such is represented by identifying numeral 217 in **Figure 4**. This portion will be mixed by mixer 211 with the original RF signal, thus producing a distortion in the output of the mixer at the baseband frequency. Again, this distortion, being at the baseband frequency, will appear in the output signal provided on signal line 215.

5 In addition to leakage between the RF and LO input ports, another problem stems from the LO signal leaking onto and being radiated by antenna 200. This leakage is represented by identifying numeral 218 in **Figure 4**. This leakage can interfere with other similar receivers that may be present in the same geographical area since the radiated LO component is at the same frequency as the RF signals
10 received by these other receivers.

This leakage problem renders the direct conversion receiver of **Figure 4** unsuitable for use in applications such as GSM mobile wireless handsets, and other systems with large blocker suppression requirements, because the distortion introduced by the leakage is unacceptable for these applications.

15 Efforts to solve this problem have involved shielding and physical separation between the RF and LO inputs. Shielding, however, is expensive and often ineffective at the high frequencies which typically characterize current mobile wireless phones, 900 MHz or more. Moreover, physical separation is impractical for use in wireless handsets, in which space is at a premium. Port to port isolation of
20 the mixer is also a finite value which typically becomes less at higher frequencies.

The distortion introduced by leakage always results in unwanted DC at the mixer output. For GSM and some other systems, this DC is not allowed to be removed by mechanisms such as a blocking capacitor because the desired signal may itself contain DC.

25

Accordingly, there is a need for a multi-band transceiver which overcomes the disadvantages of the prior art.

There is also a need for a direct conversion receiver which overcomes the disadvantages of the prior art.

Additional objects and advantages will be apparent to those of skill in the art who practice the invention or will be set forth in the disclosure which follows.

5

RELATED APPLICATIONS

This application claims priority to U.S. Patent Application Serial No. 09/386,865, filed August 31, 1999, entitled "MULTI-BAND TRANSCEIVER UTILIZING DIRECT CONVERSION RECEIVER," and U.S. Patent Application
10 Serial No. 09/260,919, filed March 2, 1999, entitled "DIRECT CONVERSION RECEIVER," both of which are owned in common by the applicant hereof, and both of which are hereby fully incorporated by reference herein as though set forth in full. Furthermore, this application is related to U.S. Patent Application Serial No.
15 09/261,056, "PREPROCESSOR AND RELATED FREQUENCY TRANSLATOR," filed March 2, 1999, and U.S. Patent Application Serial No. 09/386,956, "DIRECT CONVERSION RECEIVER EMPLOYING SUBHARMONIC FREQUENCY TRANSLATOR ARCHITECTURE AND
20 RELATED PREPROCESSOR," filed August 27, 1999, both of which are owned in common by the applicant hereof, and both of which are hereby fully incorporated by reference herein as though set forth in full.

25

SUMMARY OF THE INVENTION

In accordance with the purpose of the invention as broadly described herein, there is provided a multi-band transceiver for transmitting and receiving RF signals in one of a plurality of frequency bands. Advantageously, the transceiver is configured for use in a wireless communication device, whether a mobile device or handset, or a base station or other infrastructure component. In one implementation, the transceiver is configured for the GSM and DCS bands; in another, the GSM,
5 DCS, and PCS bands.

The receiver portion of the transceiver includes a direct conversion receiver (DCR). A signal derived from a tunable local oscillator services the receiver. In addition, in one embodiment, the local oscillator is shared with an upconverter in the transmitter portion of the transceiver.
10

The direct conversion receiver includes a frequency translator having first and second ports. In one implementation, the frequency translator is a mixer. In another, it is a multiplier. A first filter is coupled to the first port, and a second filter is coupled to the second port. Preferably, the filters are integral with or inherent to the ports so that the frequency translator lacks exposed unfiltered ports. A third
15 filter is coupled to the output of the frequency translator. It is advantageously a low pass filter configured to provide as an output signal the baseband component of the signal output from the frequency translator.

In operation, one of the plurality of bands is selected. A signal derived from the output of the local oscillator is coupled to the first filtered port of the frequency translator of the DCR. The frequency f_1 of the signal is set through suitable tuning of the local oscillator such that it is about an n th order subharmonic of the carrier frequency f_2 of the signal that is to be applied to the second filtered port of the frequency translator of the DCR, wherein n is an integer greater than 1. That is to say, $f_1 \cong (1/n)f_2$, wherein n is an integer greater than 1. (For purposes of this
20
25 disclosure, use of the terms such as "about" or "approximately" or "substantially" or

the symbol " \cong " for describing frequency or timing relationships between signals and the like is intended to take account of tolerances which are acceptable in the trade, and to allow some leeway in the description of these relationships which is consistent with these tolerances when strict mathematical exactitude may not be possible.)

5 The first filter is preferably a low pass filter having a corner frequency below the selected band, and above the frequency of the n th order harmonic. In other words, the corner frequency is above f_1 and below f_2 . Consequently, it is configured to substantially attenuate the frequency f_2 to the first unfiltered port of the frequency translator. Similarly, the second filter is preferably a high pass filter having a corner frequency below the selected band, and above the frequency of the n th order
10 harmonic. Again, the corner frequency is above f_1 and below f_2 . Consequently, it is configured to attenuate the first frequency f_1 to the second unfiltered port of the frequency translator.

Through operation of these filters, the effects of leakage between the first and second ports of the frequency translator are eliminated or reduced. Leakage
15 from the first port to the second port will be at the frequency f_1 , and thus attenuated by the second filter. Similarly, leakage from the second port to the first port will be at the frequency f_2 , and thus attenuated by the first filter. Thirdly, radiation at the frequency f_1 out through the antenna will be blocked by a bandpass filter located upstream of the DCR which has a passband centered on the selected band.

20 In one embodiment, the frequency translator is a multiplier configured to multiply the signals at the first and second input ports thereof. In another embodiment, the frequency translator is a mixer configured to switch the second input to the output through a switching action which is performed at a switching or sampling rate of n times the frequency f_1 of the signal applied to the first input of the
25 mixer. By switching at n times the frequency f_1 , the mixer conserves frequency in

that more energy is packed into the baseband component of the output of the mixer output than if the switching action were performed at the frequency f_1 .

In one embodiment, the transmitter portion of the transceiver comprises a modulator coupled to an upconverter. A carrier input source provides the carrier input to the modulator. The carrier input source comprises a frequency adjuster coupled to the output of the crystal oscillator providing the reference frequency to
5 the phase locked loop which comprises the local oscillator. The frequency adjuster is configured to receive the output of the crystal oscillator, and to provide an output signal having a frequency which is equal to the frequency of the output of the crystal oscillator adjusted by a variable amount responsive to the selected frequency band. In one implementation, the frequency adjuster is a frequency multiplier.

10 In a second embodiment, the carrier input source comprises a frequency adjuster coupled to the output of the phase locked loop which comprises the local oscillator. The frequency adjuster is configured to receive the output of the phase locked loop, and to provide an output signal having a frequency which is equal to the frequency of the output of the phase locked loop adjusted by a variable amount
15 responsive to the selected frequency band. In one implementation, the frequency adjuster is a frequency divider.

In a third embodiment, the modulator is within a loop of a translation loop upconverter, and the carrier input of the modulator is derived from a downconversion frequency translator included within the loop.

20 In one configuration, the modulator is a quadrature modulator, the upconverter is a translation loop upconverter, and the carrier input source is a low frequency offset source. In one implementation, the quadrature modulator and low frequency offset source are outside the loop of the translation loop upconverter. In a second implementation, the quadrature modulator and low frequency offset source
25 are within the loop of the translation loop upconverter.

In the case of the first configuration, a low frequency offset source provides the carrier input to the quadrature modulator. The frequency of the carrier signal is a variable depending on the selected band. It is selected to be about equal to the offset frequency for the selected band, that is, the offset between the transmit and receive channels for the selected band. The translation loop upconverter includes a transmit downconversion frequency translator. The frequency translator is of the type which
5 switches or samples at n times the frequency of the signal provided at the first input thereof. The value of n for this frequency translator is the same as that for the frequency translator in the DCR of the receiver portion of the transceiver.

In the case of the second configuration, the carrier signal for the quadrature modulator is derived from the output of the frequency translator in the translation
10 loop. The loop is configured so that the frequency of the output of the frequency translator is, after suitable filtering, about equal to the frequency offset for the selected band. In that sense, the frequency translator functions as the low frequency offset source.

In both configurations, a low pass filter is inherent to or integral with the first
15 input of the frequency translator, such that the unfiltered first input is covered and not exposed. The local oscillator in the receiver portion of the transceiver is shared with the frequency translator in the translation loop upconverter in that a signal derived from the local oscillator is coupled to the filtered first input of the frequency translator. In operation, the frequency of the signal applied to this input is about an
20 n th order subharmonic of the signal applied to the second input of the frequency translator, wherein n is an integer greater than 1.

The translation loop upconverter in both configurations receives the output of the quadrature modulator and increases the carrier frequency of this output to about the appropriate frequency for transmission. This frequency is the frequency of
25

the selected receive channel in the selected band minus the frequency offset for the selected band.

In one implementation, each of the foregoing frequency translators is a mixer, with the first input port being an LO input port, and the second input port being an RF input port. In this implementation, a procedure known as half-frequency injection is utilized. According to this procedure, the frequency f_1 of the signal applied to the LO input port is $\frac{1}{2}$ of the frequency f_2 , the carrier frequency of the RF signal applied to the second port.

In one implementation example, the transceiver is configured to handle the GSM and DCS bands. In this implementation, two switchable and selectable DCRs are provided. In operation, the DCR corresponding to the selected band is selected and switched such that it is in the signal path from the baseband filter to the switch/band selector. The first DCR is preceded by a bandpass filter having a passband defined by the GSM receive band, 925-960 MHz. The second DCR is preceded by a passband defined by the DCS receive band, 1805-1880 MHz. The local oscillator in this implementation is the output of a phase locked loop (PLL). The PLL includes a fractional N synthesizer. A reference divider at the output of a crystal oscillator at 13 MHz provides the reference frequency to the PLL. The output of the PLL is tunable in the range of 450.25 MHz to 480 MHz. The output of the PLL is applied to the LO input of the mixer in the first DCR. The output of the PLL is also passed through a doubler, and the output of the doubler is applied to the LO input of the mixer of the second DCR.

In the case in which the GSM band is selected, the PLL is tuned such that the frequency of the output thereof is about $\frac{1}{2}$ the frequency of the selected channel in the GSM band. In the case in which the DCS band is selected, the PLL is tuned so that the output thereof is about $\frac{1}{4}$ the frequency of the selected channel on the DCS band. This way, through action of the doubler, the signal which is applied to the LO

input of the mixer in the DCR corresponding to the DCS band is about $\frac{1}{2}$ the frequency of the selected channel in the DCS band.

In one configuration of this implementation, a transmitter portion of the transceiver includes a quadrature modulator followed by a translation loop upconverter. A low frequency offset source provides a carrier input to the quadrature modulator at a frequency about equal to the frequency offset between the receive and transmit channels for the selected band. As discussed, the frequency offset for the GSM band is 45 MHz, that for the DC band is 95 MHz, and that for the PCS band is 80 MHz.

In one example of this configuration, the carrier input is derived by multiplication of the crystal oscillator reference frequency by a multiplication factor which depends on the selected band. For the GSM band, assuming a 13 MHz crystal oscillator reference frequency, the multiplication factor is advantageously 3, yielding a carrier offset of 39 MHz. For the DCS band, again assuming a 13 MHz crystal oscillator reference frequency, the multiplication factor is advantageously 7, yielding a carrier offset of 91 MHz.

In another example of this configuration, the carrier input is derived by dividing the output of the PLL by a division factor which depends on the selected band. For the GSM band, assuming a PLL output frequency of 450-480 MHz, the division factor is advantageously 10, yielding a carrier offset in the range of 45-48 MHz. For the DCS band, again assuming a PLL output frequency of 450-480 MHz, the division factor is advantageously 5, yielding a carrier offset in the range of 90-96 MHz.

In a second configuration of this implementation, the quadrature modulator is contained within the loop of the translation loop upconverter in that the output of the downconversion mixer in the loop provides, after suitable filtering, the carrier input

of the quadrature modulator. The loop is configured such that the carrier input to the quadrature modulator is about the frequency offset for the selected band.

In both configurations, the translation loop upconverter is configured to increase the carrier frequency of the output of the quadrature modulator so that it is at the appropriate frequency for transmission. In the case of DCS, the transmit band is 1710-1785 MHz. In the case of GSM, the transmit band is 890-915 MHz. The
5 appropriate frequency for transmission is the selected channel within the appropriate transmit band, which has a frequency equal to that of the selected channel in the receive band minus the frequency offset for the band.

In both configurations, the output of the PLL is shared by the translation-loop upconverter in that a signal derived from the output from the PLL is provided
10 to the filtered LO input of the downconversion mixer in the translation loop upconverter. In the case of the GSM band, the PLL output is applied directly to the filtered LO input of the mixer. In the case of the DCS band, the PLL output, after passage through the doubler, is applied to the LO input of the mixer.

A related method of providing full duplex transmission and reception is
15 provided which comprises the following steps: selecting a band from a plurality of bands; receiving a signal at a channel within the selected band, the channel having a frequency; directly converting the signal to a baseband signal using a first signal derived from a local oscillator signal, the first signal being an n th subharmonic of the channel frequency, wherein n is an integer greater than 1; upconverting a second
20 baseband signal to a transmission frequency; and transmitting the upconverted signal.

The subject invention also includes a direct conversion receiver system including a multiplier having first and second input ports, wherein the system is configured to reduce the effects of leakage between the first and second input ports
25 thereof such that the receiver system is capable of use in applications requiring a

high degree of isolation between the first and second input ports, such as mobile wireless handsets.

A first aspect of the subject invention comprises a direct conversion receiver system which includes a multiplier, an oscillator circuit, and a first filter. The multiplier has a first input port configured to receive a first signal at a first frequency, a second input port configured to receive a second signal at a second
5 frequency, and an output port. In one implementation, the first signal is an RF signal which is a baseband signal modulated onto a carrier signal. In this example, it should be appreciated that the first signal is not, strictly speaking, at a single frequency. The first filter is coupled to the output of the multiplier. It is configured to allow passage of the baseband component of the signal output from the multiplier,
10 but to substantially reject at least one higher frequency component.

The first input port is coupled to a second filter configured to allow passage of the first frequency but substantially reject passage of the second frequency. The second input port is coupled to a third filter, but this filter is configured to allow
15 passage of the second frequency and substantially reject passage of the first frequency. Preferably, these filters are inside or inherent to the multiplier circuit so that the multiplier does not have exposed unfiltered ports. The multiplier is configured to provide an output signal derived from the product of the filtered first and second signals.

The oscillator circuit is configured to generate the second signal at a second
20 frequency which is related to the first frequency such that the first frequency is at least approximately an integral multiple of the second frequency. In other words, the second frequency is at least approximately a subharmonic of the first frequency. This relationship can be expressed by the following mathematical relationship in which n is an integer: $f_1 \cong nf_2$. The output of the multiplier has a baseband
25 component, as well as other components at other higher frequencies. This baseband

component is isolated from the other higher frequency components through the first filter, and provided as an output of the receiver system.

A second aspect of the subject invention comprises a multiplier which performs a switching action at a rate which is n times the second frequency, where n is the integer referred to previously in relation to describing the relationship between f_1 and f_2 . The switching occurs at a rate which defines the transfer function between a first input and the output of the multiplier. Consequently, the output signal is representative of the product of the switching rate and the first signal. By providing a switching action at a rate which is n times the second frequency, energy in the output signal is conserved in the sense that more energy is packed by the multiplier into the baseband component of the output signal than if the switching action were performed at the rate of the second frequency. More specifically, by switching at n times the second frequency, the energy of the incoming signal is, at a first order level, split between the desired baseband component, and a higher frequency component. Had switching occurred at the rate of the second frequency, the energy of the incoming signal would have been split, at a first order level, between components at the first frequency plus or minus $1/n$ times the second frequency, and only higher order (and much lower amplitude) components would have appeared at baseband frequencies.

It is contemplated that the subject invention comprises the foregoing first and second aspects singly or in combination. Also provided are related methods and computer readable media.

The foregoing direct conversion receiver system reduces the effects of leakage between the first and second input ports of the multiplier. In the case in which there is leakage from the second input port to the first input port, the leakage, which is at the frequency $f_2 \cong \frac{1}{n} f_1$, will be substantially rejected by the filter

integrated with the first port, which, as discussed, is configured to substantially reject the frequency f_2 . In the case in which there is leakage from the first port to the second port, the leakage, which is at the frequency $f_1 \cong nf_2$, will be substantially rejected by the filter integrated with the second port, which is configured to substantially reject the frequency $f_1 \cong nf_2$. In both cases, the leakage will be prevented from mixing with the signal from which it originated, and will thus be precluded from generating a distortion to the baseband component of the output signal.

In the case of leakage from the second port out through the antenna, this will typically be rejected by a bandpass filter with a passband centered around the first frequency which is normally provided upstream of the multiplier, and between the multiplier and the antenna. Such a filter is normally included to select the receive band for the system. If this filter is configured to substantially reject the second frequency, it will incidentally perform the benefit of blocking leakage from the second input port, and preventing it from radiating out through the antenna. If this filter is not configured to substantially reject the second frequency, then another filter configured to substantially reject the second frequency but allow passage of the first frequency should be added upstream from the multiplier and between the antenna and multiplier.

Another advantage of the foregoing receiver system, compared to a system in which the LO frequency is at the RF carrier frequency, is a less complex, less sensitive, and less power-consuming oscillator circuit, stemming from the fact that the frequency of the output of the oscillator circuit in the foregoing system is less than that of the oscillator circuit set at the RF carrier frequency.

Additional advantages of the foregoing receiver system in relation to the receiver of **Figure 20** include the elimination of a mixer, mixer 4002, and also a

filter, BPF 4003, which is typically referred to as the IF filter. The elimination of the IF filter is particularly advantageous since it typically must be implemented off-chip. Since the remaining filters in the system can typically be implemented on-chip, the result is a more compact system.

In one implementation, the multiplier is a mixer having an RF input port, and a LO input port. The oscillator circuit is a local oscillator circuit having an output coupled to the LO input of the mixer. The RF input of the mixer receives a signal comprising a baseband signal modulated onto an RF carrier, that is, a carrier at RF frequencies. The frequency of the signal output from the local oscillator circuit is $\frac{1}{2}$ the frequency of the RF carrier. (Consequently, a receiver incorporating such a mixer is referred to as a direct conversion receiver which utilizes half-frequency injection.) The mixer in this implementation is configured to provide a switching action at a rate equal to twice the LO frequency. A low pass filter is coupled to the output port of the mixer. The output of the mixer includes a baseband component representative of the baseband signal, and a higher frequency component, that is, a component at about twice the RF frequency. The lowpass filter substantially isolates the baseband component from the high frequency component, and outputs a signal representative of the baseband component. The lowpass filter also rejects any unwanted signals outside the desired band around f_{BB} .

BRIEF DESCRIPTION OF THE DRAWINGS

The present invention is described with reference to the accompanying drawings. In the drawings, like reference numbers indicate identical or functionally similar elements, and

Figure 1a illustrates the transmit and receive frequency bands under the GSM standard;

Figure 1b illustrates the transmit and receive frequency bands under the EGSM standard;

Figure 1c illustrates the transmit and receive frequency bands under the GSM 1800 or DCS standard;

Figure 1d illustrates the transmit and receive frequency bands under the GSM 1900 or PCS standard;

5 **Figure 2** is a block diagram of an exemplary GSM network;

Figure 3 illustrates the format of a conventional TDMA frame;

Figure 4 illustrates a conventional direct conversion receiver;

Figure 5 is a block diagram of a mobile station or handset in accordance with the subject invention;

10 **Figure 6** illustrates a block diagram of a multi-band transceiver in accordance with the subject invention;

Figure 7 illustrates a direct conversion receiver frequency translator in accordance with the subject invention;

Figure 8 illustrates a first implementation of the subject invention;

15 **Figure 9** illustrates a second implementation of the subject invention;

Figure 10 illustrates a third implementation of the subject invention;

Figures 11A-F are example waveforms illustrating operation of a frequency translator in accordance with the subject invention;

20 **Figures 12A-12B** are a block diagrams of frequency translators in accordance with the subject invention;

Figure 13 is an implementation example of a frequency translator in accordance with the subject invention;

25 **Figures 14A-14B** are implementation examples of the filters integral or inherent to the input ports of a frequency translator in accordance with the subject invention;

Figure 15 illustrates, in the frequency domain, operation of a frequency translator in accordance with the subject invention;

Figure 16 is a flowchart illustrating one implementation of a method of operation of a direct conversion receiver in accordance with the subject invention;

Figure 17 is a flowchart illustrating one embodiment of a method of operation of a direct conversion receiver in accordance with the subject invention;

5 **Figures 18A-18C** are example waveforms illustrating operation of the implementation example of **Figure 13**;

Figure 19 is a flowchart illustrating one embodiment of a method of operation of a transceiver in accordance with the subject invention;

10 **Figure 20** illustrates a receiver of the type in which the baseband signal is downconverted to baseband frequencies in two steps;

Figure 21 illustrates a direct conversion receiver subject to the effects of leakage between the RF and LO ports;

Figure 22 illustrates a direct conversion receiver configured in accordance with an embodiment of the subject invention;

15 **Figures 23A-23D** are waveforms illustrating operation of the mixer of **Figure 22**;

Figure 24 is a block diagram of an exemplary embodiment of a mixer configured in accordance with the subject invention;

20 **Figure 25** illustrates an implementation example of a mixer configured in accordance with the subject invention;

Figure 26 illustrates a first embodiment of a method of operation of the subject invention; and

Figure 27 illustrates a second embodiment of a method of operation of the subject invention.

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DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENTS

A. Transceiver Architecture

1. Example Environment

In the discussion below, "GSM" refers to the extended GSM bands of 880-915 MHz for the transmit band and 925-960 MHz for the receive band; "DCS" refers to the bands of 1710-1785 MHz for the transmit band and 1805-1880 MHz for the receive band; and "PCS" refers to the band of 1850-1910 MHz for the transmit band, and 1930-1990 MHz for the receive band.

The subject invention is a multi-band transceiver for transmitting and receiving RF signals in one of a plurality of frequency bands. Advantageously, the transceiver is configured for use in a wireless communication device, such as a mobile device or handset, or an infrastructure component such as a base station or satellite. In one implementation, the transceiver is configured for the GSM and DCS bands. In another implementation, the transceiver is configured to handle the GSM, DCS, and PCS bands.

Figure 5 is a block diagram of one implementation of a mobile, wireless handset 100 incorporating a transceiver according to the present invention. Handset 100 may operate as a mobile station within a GSM network, such as a mobile station 30 within a GSM network 20 as illustrated in **Figure 2**. Handset 100 includes a baseband digital signal processor (DSP) 102, typically integrated on a single die. Baseband DSP 102 directs the overall operation of mobile station 30. It processes baseband data received from antenna 116 and transceiver 110 into an audible acoustic signal for announcement over speaker 112. DSP 102 also processes acoustic data received from microphone 114 into baseband data which is provided to transceiver 110 for transmission over antenna 116.

DSP 102 also manages system and user interface tasks via a system interface 104 and a user interface 106. System interface 104 may include suitable means for managing functions such as GSM network and modem access and subscriber

services. User interface 106 may include suitable means for inputting and displaying information, such as a keypad, display, backlight, volume control and real time clock. In one implementation, DSP 102 is housed in a 128-pin TQFP and, in another implementation, DSP 102 is housed in a 160-pin 12x12 mm Chip Array Ball Grid Array (CABGA).

5 In one implementation, baseband DSP 102 interfaces with transceiver 110, speaker 112 and microphone 114 via integrated analog IC 108. IC 108 implements an analog-to-digital converter (ADC), a digital-to-analog converter (DAC) and all signal conversions required to permit interface between DSP 102 and transceiver 110, speaker 112 and microphone 114. Typically, the ADC and DAC will be embodied in a CODEC. Microphone 114 is configured to convert acoustic signals,
10 typically those in the audio band, into analog electric signals. The signals captured by microphone 114 are decoded and digitized by the ADC in IC 108 and processed into baseband I and Q signals by DSP 102. The digital baseband I and Q signals are converted into an analog signal stream by the DAC in IC 108, and are then modulated and transmitted (via antenna 116) by transceiver 110. Conversely,
15 modulated signals captured by antenna 116 are demodulated and converted into analog baseband I and Q signals by transceiver 110, digitized by IC 108, processed by DSP 102, and converted into an analog acoustic signal by IC 108 that is announced by speaker 112. IC 108 may be implemented in a 100-pin TQFP, a 100-pin 10x10 mm CABGA package or in any other suitable housing. A power
20 management IC (PMIC) 118 is coupled to a battery 120 and integrates on a single die all power supply related functions required by handset 100.

Handset 100 includes band selection means (not shown), such as a menu selection or switch, to permit a user to select one of a plurality of possible bands. Alternatively, or in addition, the band selection means permits automatic selection of

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the appropriate band, based on a signal from a base station indicating the proper band.

Also included in the handset 100 is a channel selection means (not shown) for selection of the appropriate channel within the selected band responsive to suitable signals from the base station handling the handset at a time. For the GSM, DCS, and PCS bands, the channel is a 200 kHz slot within the selected band. The channel selection means permits selection of either or both of the transmit and receive channels. In one implementation, selection of the transmit channel implies selection of the receive channel, and selection of the receive channel implies selection of the transmit channel, since the two bear a predetermined relationship to one another. For example, for the GSM band, the receive channel is 45 MHz higher than the transmit channel; for the DCS band, the receive channel is 95 MHz higher than the transmit channel; and for the PCS band, the receive channel is 80 MHz higher than the transmit channel. In this implementation, express selection of both the transmit and receive channels is unnecessary.

The handset 100 is preferably configured to permit full duplex transmission, that is, concurrent transmission and reception over respectively the transmit and receive channels.

In one implementation, it is contemplated that the band selection means permits selection of either the GSM band or the DCS band. In another implementation, it is contemplated that the band selection means permits selection of any one of the GSM, DCS, and PCS bands. In other implementations, it is contemplated that the transceiver can be configured to accommodate other combinations of GSM bands, more than two GSM bands, or even to support other (non-GSM) standards.

2. Transceiver Introduction

Figure 6 is a general block diagram of a transceiver 110 according to the present invention. Transceiver 110 comprises a receiver portion 320, transmitter portion 321, switch/selector 306, and antenna 307.

The transmitter portion 321 comprises modulator 301, upconverter 303, and carrier input source 302. The receiver portion 320 of the transceiver 110 comprises
5 local oscillator 311, frequency adjuster 312, direct conversion receiver (DCR) 309, bandpass filter 308, low noise amplifier (LNA) 309, and baseband filter and amplifier 313.

The switch/selector 306 is capable of first and second positions depending on the mode of operation of the transceiver 110. In a transmit mode of operation,
10 switch/selector 306 couples the output of PA 304 to antenna 307 over signal line 554. In a receive mode of operation, switch/selector 306 couples the antenna 307 to bandpass filter 308 over signal line 555.

In addition, switch/selector 306 selects, responsive to either a user input or an external signal, the band of operation. Responsive thereto, switch/selector 306,
15 over signal line 556, configures the transmitter portion 321 so that it is compatible with the selected band. In addition, switch/selector 306, over signal line 557, configures the receiver portion 320 so that it is compatible with the selected band.

Modulator 300 receives the baseband signal 300, and uses it to modulate a carrier input provided by carrier input source 302. More specifically, the carrier
20 input is modulated by the baseband signal 300, and the resulting signal is the output of the modulator 301.

The frequency of the carrier input provided by carrier input source 302 is a variable which is determined responsive to the selected band. In one implementation, the frequency is set to the frequency offset of the selected band.
25 Thus, if the selected band is the GSM band, the frequency of the carrier input is

selected to be about 45MHz; if the DCS band, the frequency of the carrier input is selected to be about 95 MHz; and if the selected band is the PCS band, the frequency of the carrier input is selected to be about 80 MHz.

Upconverter 303 receives the output of the modulator and upconverts the frequency thereof to be at the frequency appropriate for transmission, that is, the selected transmit channel within the selected transmit band. Preferably, the
5 upconverter determines the frequency for transmission responsive to a signal 323 derived from the local oscillator 311 included as part of the receiver portion 320 of the transceiver. As will be seen, the frequency of signal 323 is preferably an nth order subharmonic of the selected receive channel of the selected receive band, wherein n is an integer greater than 1.

10 In one implementation, the upconverter includes a translation loop with a switchable voltage controlled oscillator (VCO) in the loop selectable from a plurality of VCOs, each corresponding to one of the bands handled by the transceiver. In operation, the VCO corresponding to the selected band is itself selected from the plurality of VCOs, and switched so that it is in the signal path extending from the
15 modulator 301 to the switch/selector 306.

The transmitter portion 321 further comprises a power amplifier (PA) 304 for amplifying the output of the upconverter 303 responsive to the output of PA regulator 305. The PA regulator 305 regulates PA 304 responsive to the PA output. More specifically, in one implementation, PA regulator 305 regulates PA 304 so that
20 the output thereof is at a predetermined level. If the level of the output of PA 304 is below the predetermined level, PA regulator 305 boosts the amplification of PA 304 so that the output thereof is at the predetermined level. Conversely, if the level of the output of PA 304 is above the predetermined level, PA regulator 305 lowers the amplification of PA 304 so that the output thereof is again at the predetermined
25 level.

In one implementation, PA 304 is switchable, and selectable from a plurality of PAs, each corresponding to one of the bands handled by the transceiver. In operation, the PA corresponding to the selected band is itself selected from a plurality of PAs, and switched into operation such that it is in the signal path from the modulator 301 to the switch/selector 306.

In the receive mode of operation, the antenna 307 is coupled by
5 switch/selector 306 to bandpass filter 308. A signal is received by the antenna 307 and applied to the bandpass filter 308. In one implementation, the filter 308 is switchable and selectable from a plurality of filters, each having a passband corresponding to one of the bands handled by the transceiver. Thus, in the case in which the GSM band is the selected band, filter 308 is selected such that the
10 passband thereof is generally coincident with the band 925-960 MHz; in the case in which the DCS band is the selected band, filter 308 is selected such that the passband thereof is generally coincident with the band 1805-1880 MHz; and in the case in which the PCS band is the selected band, filter 308 is selected such that the passband thereof is generally coincident with the band 1930-1990 MHz. In
15 operation, the filter 308 corresponding to the selected band is switched into the signal path between the baseband filter/amplifier 313 and the switch/selector 306.

The output of bandpass filter 308 is coupled to low noise amplifier (LNA) 309. In one implementation, LNA 309 is switchable and selected from a plurality of
20 LNAs, each corresponding to one of the bands handled by the transceiver. In operation, the LNA corresponding to the selected band is itself selected, and switched into service such that it is within the signal path from the baseband filter/amplifier 313 and the switch/selector 306.

3. Frequency Translator

The output of LNA 309 is coupled to direct conversion receiver (DCR) 310.
25 Direct conversion receiver 310 includes a frequency translator of the type shown in

Figure 7 and identified with numeral 438. As illustrated in Figure 7, frequency translator 438 has first and second input ports, identified respectively with numerals 431 and 430. A first filter 432 is coupled to the first port 431, and a second filter 433 is coupled to the second port 430. Preferably, the filters are integral with or inherent to the ports so that the frequency translator lacks exposed unfiltered ports. A signal 323 derived from the output of the local oscillator 311 is coupled to the first
5 filtered port 431 of frequency translator 438. More specifically, the output of local oscillator 311 is provided to frequency adjuster 312, which is configured to adjust the frequency of the signal output from the local oscillator 311. Local oscillator 311 is tunable responsive to the selected channel in the selected receive band. In one implementation, the frequency adjuster 312 is configured to adjust the frequency of
10 the output of the local oscillator 311 by either multiplying or dividing the frequency of that output by m , where m is an integer greater than or equal to 1, determined responsive to the selected band.

The frequency f_1 of the signal 323 is set through suitable tuning of the local oscillator 311, and adjustment of the frequency adjuster 312, such that the signal 323
15 is about an n th order subharmonic (wherein n is an integer greater than 1) of the carrier frequency f_2 of the signal 324, that is, the selected channel within the selected receive band. That is to say, $f_1 \cong (1/n)f_2$, wherein n is an integer greater than 1. Signal 324 is applied to the second filtered port 430 of the frequency translator 438.

The first filter 432 is preferably a low pass filter having a corner frequency
20 which is below the selected receive band, including f_2 , and above the frequency f_1 , the n th order subharmonic of f_2 . In other words, the corner frequency is set above f_1 and below f_2 . The difference between f_2 and $(1/n)f_2$ is such that a substantial level of attenuation can be achieved by filter 432 of leakage onto port 431 at the frequency f_2 . Advantageously, the level of attenuation is 88 dB or more, consistent with
25 current GSM isolation requirements.

Similarly, the second filter 433 is preferably a high pass filter having a corner frequency which is below the selected receive band, including f_2 , and above the frequency f_1 , the n th order subharmonic of f_2 . The difference between f_2 and $(1/n)f_2$ is such that a substantial level of attenuation can be achieved by filter 433 of leakage onto port 433 at the frequency f_1 . Advantageously, the level of attenuation is 88 dB or more, consistent with current GSM isolation requirements.

5 Through operation of these filters, the effects of leakage between the first and second ports of the frequency translator are eliminated or reduced. Leakage from the first port to the second port will be at the frequency f_1 , and thus attenuated by filter 433. Moreover, this leakage will be prevented from radiating out through antenna 307 by the bandpass filter 308. Similarly, leakage from the second port to
10 the first port will be at the frequency f_2 , and thus attenuated by filter 432.

 In one embodiment, the frequency translator 438 is a multiplier configured to multiply the signals at the first and second input ports thereof. In a second embodiment, frequency translator 438 is a mixer configured to switch the second input to the output through a switching action which occurs at a switching or
15 sampling rate of n times the frequency f_1 of the signal applied to the first input 431 of the mixer, wherein n is an integer greater than 1. By switching at n times the frequency f_1 , the mixer conserves frequency in that more energy is packed into the baseband component of the output of the mixer than if the switching action were performed at the frequency f_1 .

20 Such is illustrated in **Figures 15A-B** in relation to an implementation of frequency translator 438 in which the frequency translator 438 is a mixer having LO and RF input ports, and in which a procedure known as half-frequency injection is utilized. According to this procedure, the frequency f_{LO} of the signal applied to the LO input port is about $\frac{1}{2}$ the frequency f_{RF} of the signal applied to the RF input port,
25 and the mixer switches at twice f_{LO} .

Figure 15A illustrates the effect if the mixer had switched at the frequency f_{LO} . The energy 1100 of the incoming signal at the frequency f_{RF} is principally split between a component 1101 at the frequency $f_{RF}-f_{LO}$, and a component 1102 at the frequency $f_{RF}+f_{LO}$. As can be seen, little or no energy is provided at baseband frequencies, that is, low frequencies centered about 0 Hz. The foregoing is also demonstrated by the following mathematical identity:

$$5 \quad (A \cos 2\pi f_{RF} t) \times (B \cos 2\pi f_{LO} t) = 1/2 AB [\cos 2\pi (f_{RF} - f_{LO}) t] + 1/2 AB [\cos 2\pi (f_{RF} + f_{LO}) t]$$

Since $f_{LO} \cong 1/2 f_{RF}$, the first of the foregoing components is about at the frequency $1/2 f_{RF}$ or f_{LO} , while the second of the foregoing components is about at the frequency $3/2 f_{RF}$ or $3f_{LO}$. As can be seen, there are no first order components at baseband frequencies.

10 **Figure 15B** illustrates the effect of switching at twice the frequency f_{LO} . As can be seen, the energy 1103 of the incoming signal at the frequency f_{RF} is principally split between a component 1104 at baseband frequencies, and a component 1105 at the frequency $2f_{RF}$. As can be seen, a substantial baseband component is introduced by switching the mixer at the frequency $2f_{LO}$.

15 A method of operation of a frequency translator in accordance with the subject invention is illustrated in **Figure 17**. As indicated, in step 2000, a first input signal is provided at a first frequency, and in step 2001, a second input signal is provided at a second frequency which is about $1/n$ times the frequency of the first input signal, wherein n is an integer greater than 1. In step 2002, the first input
20 signal is filtered to substantially attenuate any components at the second frequency, and in step 2003, the second input signal is filtered to substantially attenuate any components at the first frequency.

In step 2004, the filtered first signal is frequency translated by switching this signal to an output through a switching action which is performed at n times the
25 second frequency. In one implementation, the output which results is representative

of the product of a multiplication factor which switches at n times the second frequency and the filtered first signal.

As discussed, in one implementation, the frequency translator is a mixer wherein the first input thereof is the LO input of the mixer, and the second input thereof is the RF input of the mixer. In one implementation example, in accordance with half-frequency injection, the LO frequency applied to the LO input of the mixer is about $\frac{1}{2}$ the RF frequency applied to the RF input of the mixer.

The operation in the time domain of implementations of frequency translators in accordance with the subject invention can be explained further with reference to **Figures 11A-11F**. **Figure 11A** is an example of a LO signal applied to the LO input of the frequency translator, and **Figure 11C** is an example of a RF signal applied to the RF input of the frequency translator. As can be seen, in this embodiment, the frequency of the LO signal is about $\frac{1}{2}$ that of the RF signal.

Figure 11B is a multiplication factor which defines, in one implementation, the transfer function between the incoming RF signal of **Figure 11C** and the output signal, illustrated in **Figure 11D**. As can be seen, the frequency of the switching action of the multiplication factor is twice that of the LO frequency. The product of the multiplication factor and the RF signal defines, in one implementation, the output signal of **Figure 11D**.

Figure 12A illustrates a block diagram of this implementation of a frequency translator. In this implementation, LO source 607 is coupled to low-pass filter (LPF) 609, and RF source 600 is coupled to high-pass filter (HPF) 608. The output of LPF 609 is input to circuit block 606, which controls SPDT switch 603 through signal line 602, causing it to switch at a frequency which is twice the LO frequency.

The output of HPF 408 is coupled to +1 multiplication block 610 and -1 multiplication block 611. When the switch 603 is in the up position, the output of +1 multiplication block 610 is provided to the output 605, and when the switch is in

the down position, the output of -1 multiplication block 611 is provided to the output 605. Consequently, a signal is produced at output 605 which is representative of the product of a multiplication factor, which switches between $+1$ and -1 at a frequency which is twice the LO frequency, and the filtered RF signal output from HPF 608.

It is important to note that a signal at the frequency of the multiplication factor is not actually produced as a signal at a pin or node of the mixer. As one of skill in the art would appreciate, it would be counterproductive to actually produce such a signal on a pin or node of the mixer since the objective of this implementation is to prevent self-mixing of the LO signal, and production of a signal at a pin or node at twice the LO frequency would defeat that objective. Instead, in this implementation, the multiplication factor simply represents (1) a switching action which occurs at about twice the LO frequency; and (2) the transfer function between the incoming filtered RF signal and the output signal.

Figures 11E and 11F illustrate an example of a differential output signal provided in another implementation of a frequency translator of the subject invention. The LO input to the frequency translator in this example is assumed to be the signal illustrated in Figure 11A, and the RF input to the frequency translator in this example is assumed to be the signal illustrated in Figure 11C. In this implementation, the differential output signal has a positive phase component, OUT^+ , which is illustrated in Figure 11E, and a negative phase component, OUT^- , which is illustrated in Figure 11F. As illustrated, the difference between OUT^+ and OUT^- in this example is identical to the signal OUT illustrated in Figure 11D in relation to the other implementation.

Figure 12B is a block diagram of the foregoing implementation of a frequency translator in which a differential mode output is provided. Compared to Figure 12A, like elements are identified with like reference numerals in Figure

12B. As illustrated, input port 627 is provided to receive an RF signal, and input port 628 is provided to receive an LO signal. The frequency of the LO signal in this implementation is assumed to be about $\frac{1}{2}$ the frequency of the RF signal.

HPF 608 is configured to filter the RF input signal, and LPF 609 is configured to filter the LO input signal, in the manner described previously. The output of LPF 609 is provided as an input to circuit block 635, which controls SPDT switch 633 through signal line 634. SPDT switch 633 is configured to alternate, at a frequency about twice the frequency of the LO input, between switching the filtered RF input to the positive phase component of the output, OUT^+ , and the negative phase component of the output, OUT^- . The signals illustrated in **Figures 11D-11E** are examples of signals that result from this operation.

Comparing the implementation examples of **Figures 12A-12B**, it can be seen that both switch the RF input to an output, whether single-ended or a differential mode output, through a switching action which occurs at about the LO frequency.

It should be appreciated that the implementation examples illustrated in **Figures 12A-12B** are easily generalizeable to the case in which the LO input is about an n th order subharmonic of the RF input, where n is any integer greater than 1. In that case, the frequency of the LO input is about $1/n$ times the frequency of the RF input, and the frequency of the switching action which is represented by SPDT switches 603 and 633 is increased to be n times the frequency of the LO input. A method of operation of one implementation of a frequency translator in accordance with the subject invention is illustrated in **Figure 16**. As illustrated, in step 1300, an RF input is provided, and in step 1301, an LO input is provided at a frequency which is about $\frac{1}{2}$ of the RF frequency. In step 1302, the LO signal is filtered to substantially filter out any components at the RF frequency. In step 1303, the RF signal is filtered to substantially filter out any components at the LO frequency. In step 1304, the filtered RF signal is frequency translated by switching it to an output

through a switching action which occurs at twice the LO frequency. In one implementation example, the resulting output signal is representative of the product of a multiplication factor, which switches between +1 and -1 at a frequency which is twice the LO frequency, and the filtered RF signal.

As discussed previously, the multiplication factor does not represent an actual signal produced by the frequency translator of the subject invention. Instead,
5 it simply represents, in one implementation, the switching action which occurs within the frequency translator and also represents, in one implementation, the transfer function between the incoming RF signal and the output signal.

An implementation example of a mixer utilizing half-frequency injection in accordance with one embodiment of the subject invention is illustrated in **Figure 13**.
10 The mixer in this example comprises RF input block 700, LO input block 701, diode block 702, and output block 703. As shown, the RF and LO input blocks are coupled through a series connection to diode block 702, which comprises two diodes coupled back-to-back. The output of the diode block is then coupled to output block 703 which, in this example, includes a low pass filter to low pass filter the output of
15 the diode block. In this example, since the LO frequency is about $\frac{1}{2}$ of the RF frequency, a switching action is provided at twice the LO frequency by the diode block 702.

Figures 18A-18C illustrate simulated waveforms for this implementation example. **Figure 18A** illustrates the LO signal provided as an input to block 701;
20 **Figure 18B** illustrates the RF signal provided as an input to block 700; and **Figure 18C** illustrates the output signal provided as an output from block 703. As can be seen, the output signal has a component at the LO frequency, and a low frequency component. The low frequency component is the desired signal. In an actual implementation, the low pass filter in output block 703 would be configured to filter
25 out the LO frequency component.

An implementation example of RF and LO input blocks incorporating filters to reduce the effects of leakage between the RF and LO inputs is illustrated in **Figures 14A-14B**. **Figure 14A** illustrates an LO input block integrated with a low-pass filter configured to substantially eliminate RF frequencies. It can replace the LO input block in the foregoing mixer example of **Figure 13** at the line B-B' there illustrated.

5 **Figure 14B** illustrates an RF input block integrated with a high pass filter configured to substantially eliminate LO frequencies. It can replace the RF input block in the foregoing mixer example of **Figure 13** at the line A-A' there illustrated.

4. Transceiver—Detailed Discussion

Turning back to **Figure 6**, in one implementation, DCR 310 is switchable
10 and selectable from a plurality of DCRs, each corresponding to one of the bands handled by the transceiver. More specifically, in this implementation, the corner frequency of the LPF coupled to the first input port of the frequency translator in a DCR, and that of the HPF coupled to the second input port of the frequency
15 nth subharmonic of the band, wherein n is an integer greater than 1. In operation, the DCR corresponding to the selected band is selected and switched such that it is in the signal path from the baseband filter 313 and the switch/selector 306.

A method of operation of the transceiver 110 of **Figure 6** will now be described. First, a receive band is selected, and a channel within the receive band is
20 also selected. The local oscillator 311 is then tuned and/or the frequency adjuster 312 is adjusted such that the frequency of the signal 323 is set to an nth order subharmonic of the selected receive channel frequency, wherein n is an integer greater than 1. Meanwhile, in one implementation, the frequency of the carrier input source 302 is set so that it is about equal to the frequency offset for the selected
25 band.

Operation then switches back and forth between a transmit mode of operation and a receive mode of operation, with the frequency sufficient to support full-duplex transmission, that is, concurrent transmission and reception. Assuming the TDMA frame format of **Figure 3** is applicable, in which each time slot is .577 mS in duration, and there are four receive time slots followed by four transmit time slots, the transceiver 110 will switch back and forth between the transmit and receiver modes every 2.308 mS.

In the receive mode of operation, a signal is received from antenna 307 and bandlimited by bandpass filter 308 so that is limited to the selected band. The signal is amplified by LNA 309 and then input to DCR 310. DCR 310 downconverts the signal 324 to baseband frequencies in a single step. The resulting output signal 435 from DCR 323 is then passed through baseband filter and amplifier 313. The result is the baseband receive signal 314.

In the transmit mode of operation, the baseband transmit signal 300 is used to modulate a carrier signal provided by carrier input source 302. In one implementation, the carrier signal is at about the frequency offset for the selected band. The resulting output signal is then upconverted to the transmit frequency by upconverter 303 responsive to the signal 323 output from frequency adjuster 312. In one implementation, the transmit frequency f_4 bears the following relationship to n , the order of subharmonic represented by f_1 , the frequency of signal 323, f_1 , and f_3 , the frequency of the signal provided by carrier input source 302: $f_4 \cong nf_1 - f_3$. The resulting signal, after amplification by power amplifier 305, is then transmitted by antenna 307.

Although carrier input source 302, modulator 301, and upconverter 303 are shown as separate blocks or elements in **Figure 6**, it should be appreciated that embodiments are possible in which one or more of these elements or blocks are combined together. For example, configurations are possible in which upconverter

303 comprises a translation loop, and modulator 301 and carrier input source 302 are included within the translation loop.

A first implementation of a transceiver in accordance with the subject invention is illustrated in **Figure 8** in which, compared to **Figure 6**, like elements are referenced with like identifying numerals. This implementation is configured to handle full duplex communication in the GSM and DCS bands.

5 In this implementation, element 306 comprises a T_x/R_x switch 306 integrated with a band selector. Element 306 functions to couple antenna 307 to signal line 550 if a transmit mode of operation for the GSM band is selected; to signal line 551 if a transmit mode of operation for the DCS band is selected; to signal line 552 if the receive mode for the GSM band is selected; and to signal line 553 if the receive
10 mode for the DCS band is selected.

 The receive portion of the transceiver comprises local oscillator 311, doubler 312, DCRs 310a and 310b, LNAs 309a and 309b, bandpass filters 308a and 308b, and baseband gain and filter chain 313. The transmit portion of the transceiver comprises low frequency offset source 302, quadrature modulator 301, translation
15 loop upconverter 303, Pas 304a and 304b, and power control and detector 305.

 Local oscillator 311 comprises a phase-locked loop (PLL) which includes a crystal oscillator 515 as the source of the reference frequency, a reference divider 580, a phase-frequency detector (PFD) 516, a loop filter 517, a voltage-controlled oscillator (VCO) 518, and fractional N synthesizer 529. The crystal oscillator in this
20 implementation provides an output at 13 MHz. The reference divider 580 is configured to divide by 13. The loop filter 517 is configured to achieve a given lock range and/or damping factor in accordance with known techniques. The VCO is configured to provide an output signal having a frequency which ranges from 450.25 MHz-480 MHz in 50 kHz increments, generally equal to about ½ of the GSM
25 receive band or about ¼ of the DCS receive band.

The fractional N synthesizer includes a dual modulus counter set to divide by any number in the range of 450.25 to 480 in increments of .05. Preferably, the synthesizer includes a dual modulus counter which divides by a weighted average of N and N+1, with the weighting specified by the parameters A and B in accordance with the formula:

$$\left(\left(\frac{A}{A+B} \right) \times N \right) + \left(\left(\frac{B}{A+B} \right) \times (N+1) \right)$$

Thus, to obtain a divide ratio of 450.35, N would be set equal to 450, A to 65, and B to 35. The frequency of the output of VCO 518 is the product of this divide ratio and the reference frequency of 1 MHz. In operation, the values of N, A, and B are set responsive to the selected channel. In the case in which the selected band is the GSM band, these parameters are set so that the output of the VCO is about 1/2 of the channel frequency. In the case in which the selected band is the DCS band, these parameters would be set so that the output of the VCO is about 1/4 of the channel frequency.

The output of VCO 518 on signal line 519 is provided to frequency doubler 312. It is also provided to the LO input 561a of DCR 310a. It is further provided as an input to filter 512 of translation loop upconverter 303. Frequency doubler 312 doubles the frequency of the output of VCO 518, and provides the same to the LO input 561b of DCR 310b, and to the input of filter 513 of translation loop upconverter 303.

DCRs 310a and 310b are both direct conversion receivers of the type discussed previously. Both have LO inputs, respectively identified with numerals 561a and 561b, and RF inputs, respectively identified with numerals 560a and 560b. LPF 525 is integral with the LO input 561a of DCR 310a, and LPF 570 is integral with the LO input 561b of DCR 310b. In one implementation example, LPF 525 has a corner frequency of 500 MHz, and LPF 570 has a corner frequency of 1GHz. HPF 521 is integral with the RF input 560a of DCR 310a, and HPF 526 is integral

with the RF input 560b of DCR 310b. In one implementation example, the corner frequency of HPF 521 is .85 GHz, and the corner frequency of HPF 526 1.7 GHz.

Both are implemented as quadrature demodulators. Thus, DCR 310a includes two mixers, 522 and 523, and DCR 310b includes two mixers, 527 and 528. Each of these mixers has an LO input and an RF input, and each is configured to switch at twice the frequency of the signal provided at the LO input. The LO input of mixer 522 is derived from the output of VCO 518. The signal applied to the LO input of mixer 522 is phase shifted by 90° by phase-shifter 524, and then provided as the LO input to mixer 523. The output of mixer 522 is the I input to baseband gain and filter chain 313 when the GSM band is selected.

The signal applied to the LO input to mixer 527 is derived from the output of doubler 312. This signal is phase shifted by 90° by phase shifter 529, and applied to the LO input of mixer 528. The output of mixer 527 becomes the I input to baseband gain and filter chain 313, and the output of mixer 528 becomes the Q input thereof, in the case in which the DCS band is the selected band.

Signal line 552 from element 306 is input to bandpass filter 308a, which has a passband generally coincident with the GSM receive band. In one implementation example, the passband of filter 308a is the GSM receive band of 925-960 MHz. The output of filter 308a is provided as an input to LNA 309a, which is suitable for use with the GSM band. The output of LNA 309a is applied to the RF input 560a of DCR 310a.

Signal line 553 from element 306 is input to bandpass filter 308b, which has a passband generally coincident with the DCS receive band of 1805-1880 MHz. The output of filter 308b is provided as an input to LNA 309b, which is suitable for use with the DCS band. The output of LNA 309b is applied to the RF input 560b of DCR 310b.

Low frequency offset (LCO) source 302 comprises a frequency frequency translator 531 which provides a carrier input to quadrature modulator 301 at a frequency of 39 MHz in the case in which the selected band is GSM, and 91 MHz in the case in which the selected band is DCS. The 39 MHz frequency is obtained by multiplying the 13 MHz crystal oscillator frequency by 3. The 91 MHz frequency is obtained by multiplying the 13 MHz crystal oscillator frequency by 7. These
5 frequencies are about equal to the frequency offsets between the transmit and receive channels for the selected band, 45 MHz in the case of GSM, and 95 MHz in the case of DCS.

Quadrature modulator 301 comprises mixers 500 and 501, summer 502, and phase shifter 503. Mixer 500 receives the I component of the baseband signal 300 to
10 be transmitted, and multiplies it by the signal provided by LCO source 302. Mixer 501 receives the Q component of the baseband signal 300 to be transmitted, and multiplies it by a 90° phase-shifted version of the signal output from LCO source 302. This phase shifted signal is provided by phase shifter 503. The outputs of the two mixers are added by summer 502 to form the output signal of the quadrature
15 modulator 301.

The output of the quadrature modulator 301 is then provided as an input to translation loop upconverter 303. Translation loop upconverter 303 comprises filter 504, phase detector 505, loop filter 506, VCO 507, VCO 508, multiplexor 509, downconversion mixer 510, and filters 511, 512, and 513.

20 The output of quadrature modulator 301 is provided as an input to filter 504. Filter 504 functions to suppress the third harmonic of the transmission intermediate frequency. The output of filter 504 is provided as an input to phase detector 505. The other input to phase detector 505 is the output of filter 511. Phase detector 505 compares the phase of the signals provided at its two inputs, and outputs a signal
25 having a magnitude proportional to the phase difference between the two input

signals. The output of phase detector 505 is filtered by loop filter 506, and then provided as an input to VCOs 507 and 508.

VCO 507 is configured to output a signal having a frequency in the range of the DCS transmit band, 1710-1785 MHz, with the precise output frequency being determined responsive to the signal output from filter 507. VCO 508 is configured to output a signal having a frequency in the range of the GSM transmit band, 890-
5 915 MHz, with the precise output frequency being determined responsive to the signal output from filter 507.

The outputs of VCOs 507 and 508 are input to multiplexor 509 which selects one of these two signals based on which band is the selected band, and applies the selected signal to the RF input of mixer 510. If the DCS band is the selected band,
10 the output of VCO 507 is selected; if the GSM band is the selected band, the output of VCO 508 is selected.

Filter 512 is a low pass filter which receives as an input the output of VCO 518. In one implementation example, the corner frequency of filter 512 is 500 MHz. Filter 513 is a low pass filter which receives as an input the output of doubler 312.
15 In one implementation, the corner frequency of filter 513 is 1 GHz

The outputs of filters 512 and 513 are input to multiplexor 571 which selects one of these inputs based on which band is the selected band, and applies the selected signal to the RF input of mixer 510. The filters 512 and 513 are integral with the RF input of mixer 510 so that mixer 510 lacks any exposed unfiltered ports.
20 Mixer 510 is configured to switch at twice the frequency of the signal provided at the LO input thereof. If the GSM band is the selected band, the output of filter 512 is provided to the LO input of mixer 510. If the DCS band is the selected band, the output of filter 513 is provided to the LO input of mixer 510.

The frequency of the signal applied to the LO input of mixer 510 is about $\frac{1}{2}$
25 the selected receive channel frequency for the selected band. More specifically, in

the case in which DCS is the selected band, the LO frequency is set to $(F_{TX}+91\text{MHz})/2$. In the case in which GSM is the selected band, the LO frequency is set to $(F_{TX}+39\text{MHz})/2$. The output of mixer 510 will have two principal components, one at about the low frequency offset for the selected band, and the other at a much higher frequency. Filter 511 is a filter which receives the output of mixer 510 and attenuates the higher frequency component. The remaining
5 component, at about the frequency offset for the selected band, is provided as an input to phase detector 505.

In the case in which the DCS band is the selected band, the output of VCO 507 is at the frequency of the selected transmit band, which is the selected receive band minus the frequency offset for the selected band. The output of VCO 507 is
10 provided as an input to PA 304a. Power control and detector 305 controls the level of amplification provided by PA 304a so that the power of the signal output by PA 304a is at a predetermined level. The output of PA 304a is then provided as an input to element 306 over signal line 551. Element 306, as discussed, coupled signal line 551 to antenna 307 in the case in which the selected band is the DCS band, and the
15 transmit mode of operation is in effect.

In the case in which the GSM band is the selected band, the output of VCO 508 is at the frequency of the selected transmit band, which is the selected receive band minus the frequency offset for the selected band. The output of VCO 508 is provided as an input to PA 304b. Power control and detector 305 controls the level
20 of amplification provided by PA 304b so that the power of the signal output by PA 304b is at a predetermined level. The output of PA 304b is then provided as an input to element 306 over signal line 550. Element 306, as discussed, couples signal line 550 to antenna 307 in the case in which the selected band is the GSM band, and the transmit mode of operation is in effect.

25

The general operation of the implementation of **Figure 8** will now be described. The receive mode of operation will first be described, followed by the transmit mode of operation.

In the case in which the selected band is the GSM band, the division ratio of the fractional N synthesizer 519 is set such that the output of VCO 518 is at a frequency which is about $\frac{1}{2}$ the selected receive channel frequency.

5 A signal is received over antenna 307 and provided to filter 308a by element 306. Filter 308a bandlimits the signal so that it is within the GSM receive band of 925-960 MHz, and LNA 309a amplifies the signal. DCR 310a downconverts the signal to baseband frequencies in a single step while filters 521 and 525 suppress the effects of any leakage between the LO and RF inputs of the mixers 522 and 523.
10 These mixers function by switching at twice the frequency applied to the LO inputs thereof, which is about the selected channel frequency.

 The resulting I and Q signals are input to baseband gain and filter chain 313 which attenuates the components of the I and Q signals at twice the selected channel frequency, leaving the I and Q components at baseband frequencies, identified with
15 numeral 314 in the figure.

In the case in which the selected band is the DCS band, the division ratio of the fractional N synthesizer 519 is set such that the output of VCO 518 is at a frequency which is about $\frac{1}{4}$ the selected receive channel frequency.

 A signal is received over antenna 307 and provided to filter 308b by element
20 306. Filter 308b bandlimits the signal so that it is within the DCS receive band of 1805-1880 MHz, and LNA 309b amplifies the signal. DCR 310b downconverts the signal to baseband frequencies in a single step while filters 526 and 570 suppress the effects of any leakage between the LO and RF inputs of the mixers 527 and 528.
 These mixers function by switching at twice the frequency applied to the LO inputs
25 thereof, which is the selected channel frequency.

The resulting I and Q signals are input to baseband gain and filter chain 313 which attenuates the components of the I and Q signals at about twice the selected channel frequency, leaving the I and Q components at baseband frequencies, identified with numeral 314 in the figure.

In the transmit mode of operation, in the case in which the selected band is the GSM band, signal line 550 is coupled to antenna 307 by element 306.

5 Frequency multiplier 531 is set so that the multiplication factor thereof is 3. The output thereof, at the frequency 39 MHz, is applied to the carrier input of quadrature modulator 301. Quadrature modulator 301 modulates the carrier input thereof with the I and Q components of the signal 300 to be transmitted. The carrier frequency of the output of the quadrature modulator 301 is 39 MHz, about the
10 frequency offset of the GSM band.

The output of the quadrature modulator is provided to translation loop upconverter 303. Translation loop upconverter 303 functions to upconvert the frequency of the signal so that it is at the selected transmit channel frequency. The loop functions as follows. The output of VCO 508 is coupled to mixer 510 through
15 multiplexor 509. Phase detector 505 adjusts its output until the phase of the signals at its two inputs are about the same. The effect of this is to adjust the frequency of the output of VCO 508 until this phase relationship is present. This will occur when the frequency at the output of the VCO 508 is equal to twice the frequency of the signal applied to the LO input of mixer 510 (which is about the selected receive
20 channel frequency) minus 39MHz. More precisely, the frequency of the signal applied to the LO input of mixer 510 is $(F_{TX} + 39\text{MHz})/2$, where F_{TX} is the transmit frequency. As is desired, this frequency is about equal to the selected receive channel frequency minus the frequency offset for the band.

25 In the case in which the selected band is the DCS band, signal line 551 is coupled to antenna 307 by element 306.

Frequency multiplier 531 is set so that the multiplication factor thereof is 7. The output thereof, at the frequency 91 MHz, is applied to the carrier input of quadrature modulator 301. Quadrature modulator 301 modulates the carrier input thereof with the I and Q components of the signal 300 to be transmitted. The carrier frequency of the output of the quadrature modulator 301 is 91 MHz, about the frequency offset of the DCS band.

5 The output of the quadrature modulator is provided to translation loop upconverter 303. Translation loop upconverter 303 functions to upconvert the frequency of the signal so that it is at the selected transmit channel frequency. The loop functions as follows. The output of VCO 507 is coupled to mixer 510 through multiplexor 509. Phase detector 505 adjusts its output until the phase of the signals
10 at its two inputs are about the same. The effect of this is to adjust the frequency of the output of VCO 507 until this phase relationship is present. This will occur when the frequency at the output of the VCO 507 is equal to twice the frequency of the signal applied to the LO input of mixer 510 (which is about the selected receive channel frequency) minus 91MHz. More precisely, the signal applied to the LO
15 input of mixer 510 is $(F_{TX} + 91\text{MHz})/2$, where F_{TX} is the transmit frequency. As is desired, this frequency is about equal to the selected receive channel frequency minus the frequency offset for the band.

 A second implementation of the subject invention is illustrated in **Figure 9**. This implementation is identical to that of **Figure 8**, except that LOC source 302 is
20 different. In **Figure 9**, LCO source 302 comprises frequency divider 580. Frequency divider 580 receives as an input the output of VCO 518, and divides its frequency by a variable divide ratio determined responsive to the selected band. It outputs a signal having the divided down frequency. In the case in which the GSM band is selected, the division ratio is 10, and the frequency of the output signal will
25 range from 45.00-48.00 MHz, about the frequency offset for the GSM band. In the

case in which the DCS band is the selected band, the division ratio is 5, and the frequency of the output signal will range from 90.00-96.00 MHz, about the frequency offset for the DCS band. Except for this difference, the implementation of **Figure 9** is identical to that of **Figure 8**.

A third implementation of the subject invention is illustrated in **Figure 10**. This implementation is identical to that of **Figure 8**, except that both quadrature modulator 301 and LCO source 302 have been placed within the loop of translation loop upconverter 303, the inputs of phase detector 505 have been changed, and frequency divider 532 has been added to the loop. More specifically, in the implementation of **Figure 10**, the output of quadrature modulator 301 is coupled to frequency divider 532.

Frequency divider 532 divides the frequency of the output of the quadrature modulator 301 by a variable division ratio which depends on the selected band. If the selected band is the GSM band, the division ratio is 3; if the DCS band is the selected band, the division ratio is 7. The output of frequency divider 301 is coupled to filter 504, which has been previously discussed. The output of the filter 504 is coupled to an input of phase detector 505. The other input to phase detector 505 is the 13 MHz output of crystal oscillator 515. The phase detector 505 compares the phases of the two inputs, and adjusts its output until the two are about equal.

The LCO source 302 in this implementation is the output of mixer 510 after passage through the filter 511. When the loop is locked, this output will be at a frequency equal to the division ratio applied by frequency divider 532 multiplied by the 13 MHz reference frequency output from crystal oscillator 515. In the case of the GSM band, that will be 39 MHz; in the case of the DCS band, that will be 91 MHz. This frequency is divided down by divider 532 such that the output of filter 504, when input to phase detector 505, is at 13 MHz. Otherwise, the operation of the loop is as described in relation to the implementation of **Figure 8**.

A related method of providing full duplex transmission and reception is illustrated in **Figure 19**. In step 3000, a frequency band is selected from a plurality of bands. In one implementation, the band is selected from the GSM and DCS bands. In another implementation, the band is selected from the GSM, DCS, and PCS bands. In step 3001, a signal is received at a channel within the selected band. In step 3002, the received signal is directly converted to a baseband signal using a first signal which is an n th order subharmonic of the channel frequency, wherein n is an integer greater than 1. In step 3004, a second baseband signal is upconverted to a transmission frequency. In one implementation, this steps comprises the substeps of modulating a carrier signal with the second baseband signal, and then upconverting the modulated signal to the transmission frequency. In one implementation example, the carrier signal is at a frequency about equal to the frequency offset for the selected band, and the transmission frequency is about equal to the frequency of the received signal minus the frequency offset for the selected band.

The foregoing architecture is advantageous because it greatly reduces the cost of a dual-band transceiver by requiring only one main oscillator for the whole system. Furthermore, the oscillator operates at about $\frac{1}{2}$ frequency (GSM band) or about $\frac{1}{4}$ frequency (DCS band), and needs to tune over a narrow range (about 30 MHz), thus allowing for better VCO phase performance. The usual VHF oscillator and synthesizer are eliminated. Furthermore, it makes use of direct conversion in the receiver, which avoids the usual leakage problems between the LO and RF ports of the mixer, and eliminates the IF mixer.

B. Direct Conversion Receiver

The subject invention also includes a direct conversion receiver independent of the foregoing architecture.

1. Preliminary Discussion re: Direct Conversion Receivers

A direct conversion receiver, that is, a receiver in which the baseband portion of the incoming RF signal is downconverted to baseband frequencies in a single step, is illustrated in **Figure 21**. As illustrated, the receiver of **Figure 21** comprises an antenna 4100 coupled to the RF input port 4119 of mixer 4111. Mixer 4111 has a LO input port 4114, and an output port 4101. The mixer mixes the signals provided at the RF and LO input ports, and provides the mixed signal to the output port. In the receiver of **Figure 21**, the frequency of the signal provided at the LO input port, f_{LO} , is matched to the frequency of the signal provided at the RF input port, f_{RF} , such that $f_{LO} \cong f_{RF}$. The mixed signal provided at the output port 4101 of mixer 4111 has a first order component at the baseband frequency, f_{BB} , and a first component at twice the LO or RF frequencies, or $2f_{LO}$.

The output port 4101 of mixer 4114 is coupled to LPF 4112 through signal line 4113. The purpose of LPF 4112 is to isolate the baseband component of the signal output from mixer 4111 from the higher frequency component at the frequency $2f_{LO}$. LPF 4112 also rejects any unwanted signals outside the desired band around f_{BB} . The output of the LPF 4112 is provided on signal line 4115. It represents the baseband portion of the RF signal received over antenna 4100.

Comparing the receivers of **Figures 20** and **21**, it can be seen that an advantage of the design of **Figure 21** is the elimination of a mixer, a filter (BPF 4003), and the associated cost of these components. However, a problem with this design is its vulnerability to leakage between the signals on the RF and IF input ports of the mixer. This problem is explained further as follows.

With reference to **Figure 21**, consider the case in which a portion of the signal provided at the LO input port leaks onto the RF input port. Such is identified with reference numeral 4116 in **Figure 21**. This portion will be mixed by mixer 4111 with the original LO signal, thus producing a distortion in the output signal at

the baseband frequency. Since this distortion is at the baseband frequency, it will pass through LPF 4112, and appear in the output signal provided on signal line 4115. The result is that this output signal is distorted in relation to the baseband portion of the incoming signal received over antenna 4100.

Consider next the case in which a portion of the signal provided at the RF input port leaks onto the LO input port. Such is represented by identifying numeral 5 4117 in **Figure 21**. This portion will be mixed by mixer 4111 with the original RF signal, thus producing a distortion in the output of the mixer at the baseband frequency. Again, this distortion, being at the baseband frequency, will appear in the output signal provided on signal line 4115.

In addition to leakage between the RF and LO input ports, another problem 10 stems from the LO signal leaking onto and being radiated by antenna 4100. This leakage is represented by identifying numeral 4118 in **Figure 21**. This leakage can interfere with other similar receivers that may be present in the same geographical area since the radiated LO component is at the same frequency as the RF signals received by these other receivers.

15 This leakage problem renders the direct conversion receiver of **Figure 21** unsuitable for use in applications such as GSM mobile wireless handsets, and other systems with large blocker suppression requirements, because the distortion introduced by the leakage is unacceptable for these applications.

Efforts to solve this problem have involved shielding and physical separation 20 between the RF and LO inputs. Shielding, however, is ineffective at the high frequencies which typically characterize current mobile wireless phones, 900 MHz or more. Moreover, physical separation is impractical for use in integrated circuits, in which space is at a premium.

The distortion introduced by leakage always results in unwanted DC at the 25 mixer output. For GSM and some other systems, this DC is not allowed to be

removed by mechanisms such as a blocking capacitor because the desired signal may itself contain DC.

2. Reducing the Effects of Leakage

The subject invention comprises a direct conversion receiver configured to reduce the effects of leakage. A first embodiment of a direct conversion receiver system in accordance with the subject invention is illustrated in **Figure 22**. As shown, the system comprises antenna 4300 coupled to processing circuitry 4334. The antenna is configured to receive a first signal at a first frequency. In one implementation, the first signal is a baseband signal modulated onto an RF carrier. The processing circuitry 4334 is configured to perform certain standard processing operations on the incoming signal, including bandlimiting the incoming signal so that it is within a predetermined frequency range, which is typically the full system receive band consisting of all receive channels. In one implementation, the processing circuitry includes a bandpass filter to perform this bandlimiting task. These processing steps are known to those of skill in the art, and need not be explained further.

Also included in the first embodiment of the receiver system is a multiplier 4338 having a first input port 4330, a second input port 4331, and an output port 4339. The first input port 4330 is configured to receive the output of the processing circuitry 4334 at the frequency f_1 . In one implementation, the multiplier is a mixer, and the first input port is a RF input port. The second input port is configured to receive a second signal at a second frequency f_2 from an oscillator circuit (not shown). In one implementation, the second input port is a LO input port, the oscillator circuit is a local oscillator circuit, and the second signal is a LO signal generated by the local oscillator circuit.

The oscillator circuit is configured to generate the second signal at the second frequency f_2 which bears a relationship with the first frequency f_1 . More

specifically, according to this relationship, the first frequency f_1 is about an integral multiple of the second frequency f_2 such that $f_1 \cong n f_2$ wherein n is an integer. In one implementation, n is 2, such that the second frequency is about $\frac{1}{2}$ of the first frequency, although it is exactly $\frac{1}{2}$ the frequency of the RF carrier. In one implementation example, the second frequency is the LO frequency, the first frequency is the RF frequency, and n is 2 such that the LO frequency is about $\frac{1}{2}$ of the RF frequency. This implementation example utilizes what is known as half-frequency injection.

Also included in the system are filters 4333 and 4332. As shown, filter 4333 is coupled to the first input port 4330 of multiplier 4338, and filter 4332 is coupled to the second input port 4331 of multiplier 4338. These filters are inside or inherent to the multiplier/mixer circuit, so that the multiplier/mixer does not have exposed unfiltered ports. The purpose of these filters is to reduce the effects of leakage between the first and second input ports 4330 and 4332. The filter 4333 is configured to substantially filter out the frequency f_2 , while filter 4332 is configured to substantially filter out the frequency f_1 . In one implementation, filter 4333 is a high pass filter and filter 4332 is a low pass filter. In one implementation example, filter 4333 is integral with input port 4333 and filter 4332 is integral with input port 4331.

Multiplier 4338 is configured to multiply the first and second signals respectively appearing at the first and second input ports 4330 and 4331, after filtering by filters 4333 and 4332 respectively, and to provide the multiplied signal to output port 4339.

Multiplier 4338 is configured to multiply the first and second signals with a switching action which is n times the rate of frequency f_2 such that the baseband component at the output of the multiplier is a first order component of the output. This aspect of multiplier 4338 can be explained further with reference to **Figures**

15A-15B. With reference to **Figure 15A**, this figure illustrates the operation in the frequency domain of a conventional multiplier in which the frequency of the LO input thereof is $\frac{1}{2}$ that of the RF carrier, and the switching action in the multiplier is maintained at the LO frequency. The incoming RF signal, identified with numeral 900, is split into two first order output components, each having $\frac{1}{2}$ the energy of the incoming RF signal. The first component, identified with numeral 1101, is centered at a frequency equal to the LO frequency, or $\frac{1}{2}$ the RF frequency. The second component, identified with numeral 1102, is centered at a frequency equal to three times the LO frequency, or $\frac{3}{2}$ the RF carrier frequency. This can be seen from the following mathematical identity:

$$(A \cos 2\pi f_{RF} t) \times (B \cos 2\pi f_{LO} t) = \frac{1}{2} AB [\cos 2\pi (f_{RF} - f_{LO}) t] + \frac{1}{2} AB [\cos 2\pi (f_{RF} + f_{LO}) t]$$

The first of the foregoing components is at the frequency $\frac{1}{2}f_{RF}$ or f_{LO} , while the second of the foregoing components is at the frequency $\frac{3}{2}f_{RF}$ or $3f_{LO}$. As can be seen, there are no first order components at baseband frequencies.

With reference to **Figure 15B**, this figure illustrates the operation in the frequency domain of a multiplier configured to provide a switching action at a rate equal to twice the LO frequency in accordance with one embodiment of the subject invention. The incoming RF signal, which is identified with numeral 1103, is split into two first order output components, identified with numerals 1104 and 1105. The first component, identified with numeral 1104, is centered at baseband frequencies, and the second component, identified with numeral 1105, is centered at twice the RF frequency, or $2f_{RF}$. As can be seen, a first order component at baseband frequencies is provided in the multiplier of **Figure 15B**, but not in the multiplier of **Figure 15A**.

The operation in the time domain of a multiplier configured in accordance with one embodiment of the subject invention can be explained further with

reference to **Figures 23A-23D**. **Figure 23A** is an example of a LO signal applied to the second input of the multiplier, and **Figure 23C** is an example of a RF signal applied to the first input of the multiplier. As can be seen, the frequency of the LO signal is $\frac{1}{2}$ that of the RF signal.

Figure 23B is a multiplication factor which defines the transfer function between the incoming RF signal of **Figure 23C** and the output signal, illustrated in **Figure 23D**. As can be seen, the frequency of the switching action of the multiplication factor is twice that of the LO frequency. The product of the multiplication factor and the RF signal defines the output signal of **Figure 23D**.

Figure 12A illustrates a block diagram of one embodiment of a multiplier in accordance with the subject invention. In this embodiment, LO source 607 is coupled to low-pass filter (LPF) 609, and RF source 600 is coupled to high-pass filter (HPF) 608. The output of LPF 609 is input to circuit block 606, which controls DTSP switch 603 through signal line 602 in accordance with a multiplication factor which, in one implementation, switches between +1 and -1 at a rate which is twice the LO frequency.

The output of HPF 608 is coupled to +1 multiplication block 610 and -1 multiplication block 611. Switch 603 is configured such that, when the multiplication factor is at +1, the output of multiplication block 610 is provided to the output 605, and when the multiplication factor is -1, the output of -1 multiplication block 611 is provided to the output 605. Consequently, a signal is produced at output 605 which is representative of the product of the multiplication factor and the filtered RF signal output from HPF 608.

It is important that the multiplication factor is not actually produced as a signal at a pin or node of the multiplier. As one of skill in the art would appreciate, it would be counterproductive to actually produce such a signal on a pin or node of the multiplier since the objective of this embodiment is to provide an LO signal

which is about $\frac{1}{2}$ the RF frequency, and production of a signal at a pin or node at twice the LO frequency would defeat that objective. Instead, in this embodiment, the multiplication factor simply represents (1) a switching action which occurs at twice the LO frequency; and (2) the transfer function between the incoming filtered RF signal and the output signal.

5 A method of operation of this embodiment of the multiplier is illustrated in **Figure 26**. As illustrated, in step 5000, an RF input is provided, and in step 5001, an LO input is provided at a frequency which is about $\frac{1}{2}$ of the RF frequency. In step 5002, the LO signal is filtered to substantially filter out any components at the RF frequency. In step 5003, the RF signal is filtered to substantially filter out any components at the LO frequency. In step 5004, the filtered RF and LO inputs are
10 multiplied while a switching action is effectively performed at twice the LO frequency. In step 5005, an output signal is determined which is representative of the product of a multiplication factor which switches at twice the LO frequency and the filtered RF signal.

15 As discussed previously, the multiplication factor does not represent an actual signal determined by the multiplier of the subject invention. Instead, it represents the effective switching action which occurs within the multiplier and also represents the transfer function between the incoming RF signal and the output signal.

20 A method of operation of the embodiment of the subject invention of **Figure 22** is illustrated in **Figure 27**. As indicated, in step 6000, a first input signal is provided at a first frequency, and in step 6001, a second input signal is provided at a second frequency which is about $\frac{1}{n}$ times the frequency of the first input signal, where n is an integer. In step 6002, the first input signal is filtered to substantially filter out any components at the second frequency, and in step 6003, the second

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input signal is filtered to substantially filter out any components at the first frequency.

In step 6004, the filtered first and second signals are multiplied together while a switching action is performed at n times the second frequency. In step 6005, an output is determined which is representative of the product of a multiplication factor which switches at about n times the second frequency and the filtered first signal.

Compared to the direct conversion receiver of **Figure 21**, the foregoing direct conversion receiver system is less vulnerable to leakage between the first and second input ports of the multiplier. In the case in which there is leakage from the second input port to the first input port, the leakage, which is at the frequency

$f_2 \cong \frac{1}{n} f_1$, will be substantially rejected by the filter coupled to the first port, which,

as discussed, is configured to substantially reject the frequency f_2 . In the case in which there is leakage from the first port to the second port, the leakage, which is at the frequency $f_1 \cong n f_2$, will be substantially rejected by the filter coupled to the second port, which is configured to reject the frequency $f_1 \cong n f_2$. In both cases, the leakage will be substantially prevented from mixing with the signal from which it originated, and will thus be substantially precluded from generating a distortion to the baseband component of the output signal.

In the case of leakage from the second port out through the antenna, this will typically be rejected by a bandpass filter with a passband approximately centered around the first frequency which is normally provided upstream of the multiplier (such as block 4334 in **Figure 22**). Such a filter is normally included to select the receive band for the system. If this filter is configured to substantially reject the second frequency, it will incidentally perform the benefit of blocking leakage from

the second input port, and preventing it from radiating out through the antenna. If this filter is not configured to substantially reject the second frequency, then another filter configured to substantially reject the second frequency but allow passage of the first frequency should be added upstream from the multiplier and between the antenna and multiplier.

Another advantage of the foregoing receiver system in relation to the direct conversion receiver of **Figure 21** is a less complex oscillator circuit, stemming from the fact that the frequency of the output of the oscillator circuit in the foregoing system is less than that of the oscillator circuit implied in the design of **Figure 21**.

Additional advantages of the foregoing receiver system in relation to the receiver of **Figure 20** include the elimination of a mixer, mixer 4002, and also a filter, BPF 4003, which is typically referred to as the IF filter. The elimination of the IF filter is particularly advantageous since it typically must be implemented off-chip. Since the remaining filters in the system can typically be implemented on-chip, the result is a more compact system.

In light of the foregoing, it can be seen that a direct conversion receiver system is provided in which the effects of leakage between the first and second input ports of the multiplier thereof are reduced. The result is a direct conversion receiver which is suitable for use in GSM/DCS mobile handsets, in which there is a requirement that the leakage between the RF and LO inputs of the mixer thereof be reduced about 80-90 dB.

It should be appreciated that embodiments are possible in which the multiplier thereof provides an effective switching action at the second frequency provided the multiplier outputs a significant higher order baseband component. For example, in the case in which half frequency injection is utilized, that is, the LO frequency to the mixer is about $\frac{1}{2}$ the RF frequency, and the mixer switches at the LO frequency, the baseband component will be a second order component rather

than a first order component. If this component is substantial, a receiver system employing this mixer is a possible embodiment of the invention.

Several implementation examples of the direct conversion receiver of the subject invention will now be described.

IMPLEMENTATION EXAMPLES

EXAMPLE NO. 1

5 A first implementation example of a mixer utilizing half-frequency injection in accordance with one embodiment of the subject invention is illustrated in **Figure 13**. The mixer in this example comprises RF input block 700, LO input block 701, diode block 702, and output block 703. As shown, the RF and LO input blocks are coupled through a series connection to diode block 702, which comprises two diodes
10 coupled back-to-back. The output of the diode block is then coupled to output block 703 which, in this example, includes a low pass filter to low pass filter the output of the diode block. In this example, since the LO frequency is about $\frac{1}{2}$ of the RF frequency, a switching action is provided at twice the LO frequency by the diode block 702. **Figures 18A-18C** illustrate simulated waveforms for this
15 implementation example. **Figure 18A** illustrates the LO signal provided as an input to block 701; **Figure 18B** illustrates the RF signal provided as an input to block 700; and **Figure 18C** illustrates the output signal provided as an output from block 703. As can be seen, the output signal has a component at the LO frequency, and a low frequency component. The low frequency component is the desired signal. In an
20 actual implementation, the low pass filter in output block 703 would be configured to filter out the LO frequency component.

EXAMPLE NO. 2

 A second implementation example of a mixer utilizing half-frequency injection in accordance with one embodiment of the subject invention is illustrated
25 in **Figure 24**. As shown, the mixer in this example comprises RF input block 4602,

LO input block 4601, cross-coupled transistor block 4600, and output block 4603. The RF and LO input blocks are coupled to transistor block 4600 as shown. The LO frequency in this example is about $\frac{1}{2}$ the RF frequency. A switching action is provided by the cross-coupled transistor block 4600 at twice the LO frequency.

EXAMPLE NO. 3

A third implementation example of a mixer utilizing half-frequency injection in accordance with one embodiment of the subject invention is illustrated in **Figure 25**. As shown, the mixer in this example comprises RF input block 4702, LO input block 4701, diode block 4700, and output block 4703, coupled together as shown. The LO frequency in this example is again about $\frac{1}{2}$ the RF frequency, and a switching action at twice the LO frequency is provided by diode block 4700.

EXAMPLE NO. 4

An implementation example of RF and LO input blocks incorporating filters to reduce the effects of leakage between the RF and LO inputs is illustrated in **Figures 14A-14B**. **Figure 14A** illustrates an LO input block integrated with a low-pass filter configured to substantially eliminate RF frequencies. It can replace the LO input block in any of the foregoing mixer examples of **Figures 13, 24-25** at the line B-B' illustrated in each.

Figure 14B illustrates an RF input block integrated with a high pass filter configured to substantially eliminate LO frequencies. It can replace the RF input block in any of the foregoing mixer examples of **Figures 13, 24-25** at the line A-A' illustrated in each.

Although this invention has been described in terms of certain embodiments, implementations, and implementation examples, it should be apparent to those of skill in the art that many other embodiments, implementations, and implementation examples are within the scope of the invention. Accordingly, the scope of the invention should not be limited except in light of the claims and their equivalents.

CLAIMS

What is claimed is:

1. A multi-band transceiver comprising:
 - a band selector for selecting one of a plurality of frequency bands;
 - a receiver portion configurable responsive to the selected band;
 - 5 a transmitter portion configurable responsive to the selected band;
 - an antenna; and
 - a switch configured to couple the receiver portion to the antenna in a receive mode of operation, and to couple the transmitter portion to the antenna in a transmit mode of operation;
- 10 wherein the receiver portion includes:
 - a direct conversion receiver system for directly downconverting a signal to baseband frequencies;
 - a tunable local oscillator system for providing a first signal at a first frequency which is an n th order subharmonic of a selected channel frequency within
 - 15 the selected band, wherein n is an integer greater than 1;
 - wherein the direct conversion receiver system includes a frequency translator having a first input for receiving the first signal, a second input for receiving a second signal having a carrier frequency at the selected channel frequency, and an output, a first filter integral with the first input and configured to substantially
 - 20 attenuate signals at the selected channel frequency, a second filter integral with the second input and configured to substantially attenuate signals at the first frequency;
 - wherein the frequency translator is configured to switch the second signal to the output through a switching action which occurs at a frequency which is n times the first frequency.

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2. The transceiver of claim 1 wherein the transmitter portion includes an upconverter for upconverting a signal to a desired transmission frequency responsive to the first signal.
3. The transceiver of claim 2 wherein the transmitter portion further includes a modulator for modulating a carrier input signal responsive to a baseband signal, and
5 a carrier input source for providing the carrier input signal.
4. The transceiver of claim 3 wherein the upconverter is a translation loop upconverter having a loop.
- 10 5. The transceiver of claim 4 wherein the modulator and carrier input source are configured to be outside the loop of the translation loop upconverter.
6. The transceiver of claim 4 wherein the modulator and carrier input source are configured to be within the loop of the translation loop upconverter.
15
7. The transceiver of claim 3 wherein the carrier input source provides a carrier input signal at a frequency about equal to the frequency offset for the selected band.
8. The transceiver of claim 4 wherein the loop of the translation loop
20 upconverter includes a downconversion frequency translator having a first input for receiving the first signal, a second input for receiving a second signal at the desired transmission frequency, and an output;
wherein the frequency translator is configured to switch the second signal to the output through a switching action which occurs at a frequency which is n times
25 the first frequency.

9. The transceiver of claim 1 wherein the band selector is configured to select a band from the GSM and DCS bands.
10. The transceiver of claim 1 wherein the local oscillator system comprises a local oscillator coupled to a frequency frequency translator.
- 5 11. The transceiver of claim 1 wherein $n=2$.
12. The transceiver of claim 1 wherein the receiver portion has a receiver signal path, and wherein the direct conversion receiver is selectable from a plurality of direct conversion receivers, and switchable into a receiver signal path responsive to
10 the selected band.
13. The transceiver of claim 8 wherein the first filter of the downconversion frequency translator is selectable from a plurality of filters, and switchable into the first input of the frequency translator responsive to the selected band.
- 15 14. A wireless communication device including the transceiver of claim 1.
15. The device of claim 14 selected from the group comprising a mobile handset, base station, infrastructure component, and a satellite.
- 20 16. A wireless communication system comprising a base station and a plurality of mobile devices configured to communicate with the base station over a wireless interface, at least one of the mobile devices or base station including the transceiver of claim 1.

25

17. A multi-band transceiver comprising:

a band selector for selecting one of a plurality of frequency bands;

a receiver portion configurable responsive to the selected band;

a transmitter portion configurable responsive to the selected band;

an antenna; and

5 a switch configured to couple the receiver portion to the antenna in a receive mode of operation, and to couple the transmitter portion to the antenna in a transmit mode of operation;

wherein the receiver portion includes:

a direct conversion receiver system for directly downconverting a signal to baseband frequencies;

10 a tunable local oscillator system for providing a first signal at a first frequency which is an n th order subharmonic of a selected channel frequency within the selected band, wherein n is an integer greater than 1;

wherein the direct conversion receiver system includes a frequency translator having a first input for receiving the first signal, a second input for receiving a
15 second signal having a carrier frequency at the selected channel frequency, and an output, a first filter integral with the first input and configured to substantially attenuate signals at the selected channel frequency, a second filter integral with the second input and configured to substantially attenuate signals at the first frequency;

20 wherein the frequency translator is configured to switch the second signal to the output through a switching action which occurs at a frequency which is n times the first frequency; and

wherein the transmitter portion includes an upconverter for upconverting a signal to a desired transmission frequency responsive to the first signal.

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18. The transceiver of claim 14 wherein the upconverter is a translation loop upconverter having a loop, and the loop of the translation loop upconverter includes a downconversion frequency translator having a first input for receiving the first signal, a second input for receiving a second signal at the desired transmission frequency, and an output;

5 wherein the frequency translator is configured to switch the second signal to the output through a switching action which occurs at a frequency which is n times the first frequency.

19. A multi-band transceiver comprising:

a band selector for selecting one of a plurality of frequency bands;

10 a receiver portion configurable responsive to the selected band, the receiver portion having a receiver signal path;

a transmitter portion configurable responsive to the selected band;

an antenna; and

15 a switch configured to couple the receiver portion to the antenna in a receive mode of operation, and to couple the transmitter portion to the antenna in a transmit mode of operation;

wherein the receiver portion includes:

a direct conversion receiver system for directly downconverting a signal to baseband frequencies;

20 a tunable local oscillator system for providing a first signal at a first frequency which is an n th order subharmonic of a selected channel frequency within the selected band, wherein n is an integer greater than 1;

25 wherein the direct conversion receiver system includes a frequency translator having a first input for receiving the first signal, a second input for receiving a second signal having a carrier frequency at the selected channel frequency, and an

output, a first filter integral with the first input and configured to substantially attenuate signals at the selected channel frequency, a second filter integral with the second input and configured to substantially attenuate signals at the first frequency;

wherein the frequency translator is configured to switch the second signal to the output through a switching action which occurs at a frequency which is n times the first frequency;

5 wherein the local oscillator system includes a local oscillator and a frequency adjuster which is switchable into the receiver signal path responsive to the selected band.

20. A mobile wireless handset including the transceiver of claim 18.

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21. The transceiver of claim 18 wherein the frequency adjuster is a frequency doubler.

22. The transceiver of claim 20 wherein the frequency doubler is selectable
15 responsive to the selected band being the DCS band.

23. The transceiver of claim 18 wherein the local oscillator includes a phase
locked loop having a reference frequency provided by a crystal oscillator, the crystal
oscillator having a frequency, and the transmitter portion of the transceiver includes
20 a modulator having a carrier input, and a frequency adjuster for providing the carrier
input to the modulator, the frequency adjuster configured to receive the output of the
crystal oscillator, and to provide an output signal having a frequency equal to the
frequency of the crystal oscillator adjusted by a variable amount responsive to the
selected band.

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24. The transceiver of claim 18 wherein the local oscillator includes a phase locked loop having an output, the output having a frequency, and the transmitter portion of the transceiver includes a modulator having a carrier input, and a frequency adjuster for providing the carrier input to the modulator, the frequency adjuster configured to receive the output of the phase locked loop, and to provide an output signal having a frequency equal to the frequency of the output of the phase locked loop adjusted by a variable amount responsive to the selected band.

25. The transceiver of claim 22 wherein the frequency adjuster is a frequency frequency translator.

26. The transceiver of claim 23 wherein the frequency adjuster is a frequency divider.

27. The transceiver of claim 18 wherein the transmitter portion of the transceiver includes a modulator having a carrier input included within a loop of a translation loop upconverter, and the carrier input to the modulator is derived from a downconversion frequency translator included within the loop of the translation loop upconverter.

28. A method of performing full duplex communication in a multi-band transceiver comprising:

- selecting a band from a plurality of bands;
- receiving a signal at a channel within the selected band, the channel having a channel frequency;
- directly converting the signal to a baseband signal using a first signal having a frequency which is an nth order subharmonic of the channel frequency while

switching at n times the frequency of the first signal, wherein n is an integer greater than 1;

upconverting a second baseband signal to a transmission frequency; and
transmitting the upconverted signal.

29. The method of claim 27 wherein the upconverting step comprises:
5 modulating a carrier signal with the second baseband signal; and
upconverting the modulated signal to the transmission frequency using the
first signal.

30. The method of claim 28 further comprising modulating the second
10 basedband signal with a carrier signal which is the frequency offset for the selected
band.

31. The method of claim 27 further comprising selecting a band from the GSM
and DCS bands.

32. A direct conversion receiver system comprising:
15 a multiplier having a first input port for receiving a first signal at a first
frequency, a second input port for receiving a second signal at a second frequency
equal to about $1/n$ times the first frequency, wherein n is an integer, and an output
20 port, wherein the multiplier is configured to provide to the output port an output
signal representative of the product of the first signal and a multiplication factor
which is switched at n times the second frequency, the output signal having a
baseband component and another component;

25

an oscillator circuit for providing to the second input port the second signal at the second frequency about equal to $1/n$ times the first frequency, wherein n is an integer;

a first filter coupled to the first input port and configured to substantially filter out leakage at the second frequency;

5 a second filter coupled to the second input port and configured to substantially filter out leakage at the first frequency; and

a third filter coupled to the output port of the multiplier and configured to substantially filter out the other component, and maintain the baseband component in the output signal.

10 33. The system of claim 32 wherein n is 2.

34. The system of claim 32 wherein the first signal is an RF signal.

35. The system of claim 32 wherein the second signal is a LO signal.

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36. The system of claim 32 wherein the multiplier is a mixer.

37. The system of claim 32 wherein the first filter is integral with the first input port.

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38. The system of claim 32 wherein the second filter is integral with the second input port.

39. The system of claim 32 wherein the first filter is a high pass filter.

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40. The system of claim 32 wherein the second filter is a low pass filter.

41. The system of claim 32 wherein the third filter is a low pass filter.

42. A direct conversion receiver system comprising:

5 a multiplier having a first input port for receiving a first signal at a first frequency, a second input port for receiving a second signal at a second frequency about equal to $1/n$ times the first frequency, wherein n is an integer, and an output port, wherein the multiplier is configured to provide to the output port an output signal derived from the product of the filtered first and second signals, the output signal having a baseband component and another component;

10 an oscillator circuit for providing to the second input port the second signal at the second frequency about equal to $1/n$ times the first frequency, wherein n is an integer;

a first filter coupled to the first input port and configured to substantially filter out leakage at the second frequency;

15 a second filter coupled to the second input port and configured to substantially filter out leakage at the first frequency; and

a third filter coupled to the output port of the multiplier and configured to substantially filter out the higher frequency component, and maintain the baseband component in the output signal.

20

43. A method for performing direct conversion of a first signal comprising:

providing the first signal at a first frequency;

providing a second signal at a second frequency about equal to $1/n$ times the first frequency, wherein n is an integer;

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filtering the first signal to substantially filter out any leakage at the second frequency which may be present;

filtering the second signal to substantially filter out any leakage at the first frequency which may be present;

providing an output signal representative of the product of the filtered first signal and a multiplication factor which switches at n times the second frequency,
5 the output signal having a baseband component and a higher frequency component;
and

filtering the output signal to substantially remove the higher frequency component and maintain the baseband component in the output signal.

10 44. The method of claim 43 wherein the first signal is an RF signal.

45. The method of claim 43 wherein the second signal is an LO signal.

46. The method of claim 43 wherein n is 2.

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47. A computer readable media embodying a series of instructions to perform a method for performing direct conversion of a first signal comprising the following steps:

20 providing the first signal at a first frequency;
providing a second signal at a second frequency about equal to $1/n$ times the first frequency, wherein n is an integer;

filtering the first signal to substantially filter out any leakage at the second frequency which may be present;

25 filtering the second signal to substantially filter out any leakage at the first frequency which may be present;

providing an output signal representative of the product of the filtered first signal and a multiplication factor which switches at n times the second frequency, the output signal having a baseband component and another component; and

filtering the output signal to substantially remove the other component and maintain the baseband component in the output signal.

5 48. A direct conversion receiver system comprising:

first means for receiving a first signal at a first frequency, receiving a second signal at a second frequency about equal to $1/n$ times the first frequency, wherein n is an integer, and providing an output signal derived from the product of the filtered first and second signals, the output signal having a baseband component and another component;

10 second means for providing to the second input port the second signal at the second frequency about equal to $1/n$ times the first frequency, wherein n is an integer;

15 third means coupled to the first input port and configured to substantially filter out leakage at the second frequency;

fourth means coupled to the second input port and configured to substantially filter out leakage at the first frequency; and

fifth means coupled to the output port and configured to substantially filter out the other component, and maintain the baseband component in the output signal.

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49. A method for performing direct conversion of a first signal comprising:

providing the first signal at a first frequency;

providing a second signal at a second frequency about equal to $1/n$ times the first frequency, wherein n is an integer;

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filtering the first signal to substantially filter out any leakage at the second frequency which may be present;

filtering the second signal to substantially filter out any leakage at the first frequency which may be present;

providing an output signal derived from the product of the filtered first and second signals, the output signal having a baseband component and another component; and

filtering the output signal to substantially remove the other component and maintain the baseband component in the output signal.

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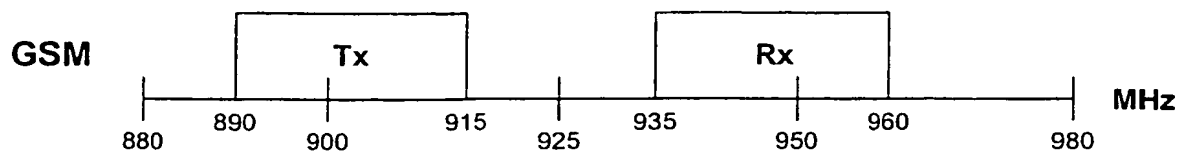
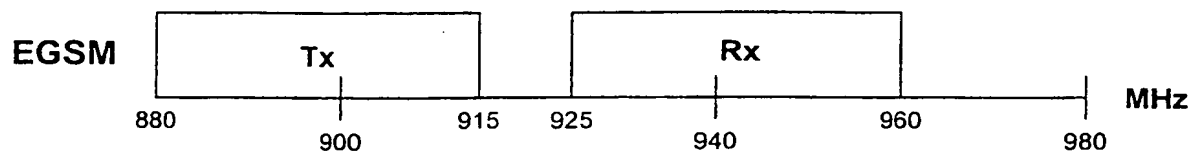
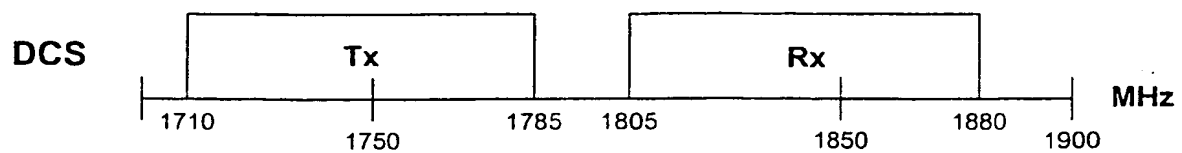
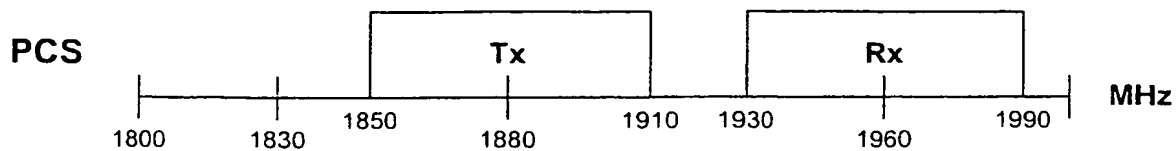
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ABSTRACT OF THE DISCLOSURE

A multi-band transceiver having a receiver portion and a transmitter portion, wherein the receiver portion includes a direct conversion receiver system for directly downconverting a signal to baseband frequencies. The direct conversion receiver system includes a frequency translator having first and second inputs and an output. A first signal at a first frequency is applied to the first input. A second signal having a
5 second frequency is applied to the second input. The first frequency is preferably an n th order subharmonic of the second frequency, wherein n is an integer greater than 1. A low pass filter is integral with or inherent to the first input, and a high pass filter is integral with or inherent to the second input. The corner frequencies of both the low pass and high pass filters is above the first frequency and below the second frequency.

10 A direct conversion receiver system is also provided in which a first input signal at a first frequency is applied to a first input port of a multiplier, a second input signal at a second frequency equal to about $1/n$ times the first frequency, wherein n is an integer, is applied to a second input port of the multiplier. A first filter coupled to the first input port is configured to substantially filter out any leakage at the second
15 frequency which may be present. A second filter coupled to the second input port is configured to substantially filter out any leakage at the first frequency which may be present. The multiplier is configured to produce a signal at an output port thereof which is derived from the product of the first and second signals. In one embodiment, the output is representative of the product of the filtered first signal and a
20 multiplication factor which switches at n times the second frequency. The output of the multiplier is coupled to a third filter. The output signal has a baseband component and another component. The third filter is configured to substantially filter out the other component and substantially maintain the baseband component in the output signal. In one implementation example, the multiplier is a mixer initializing half-
25 frequency injection, such that the LO frequency is about $1/2$ the RF frequency.

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*Figure 1a**Figure 1b**Figure 1c**Figure 1d*

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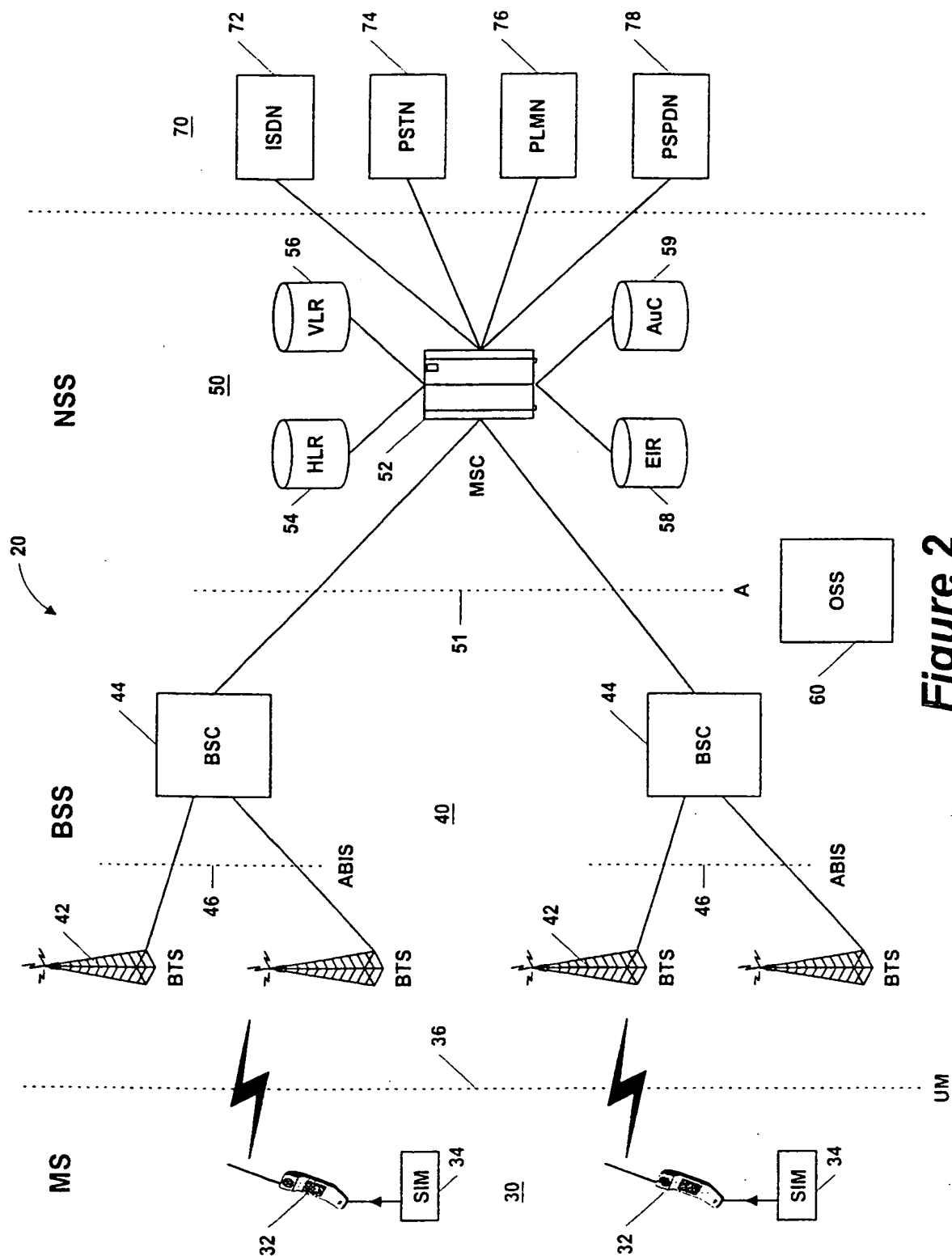
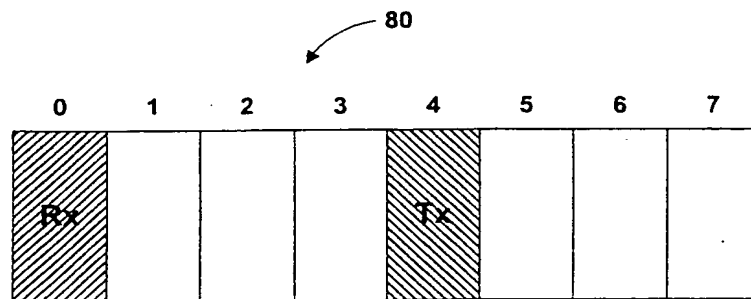


Figure 2

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**Figure 3**

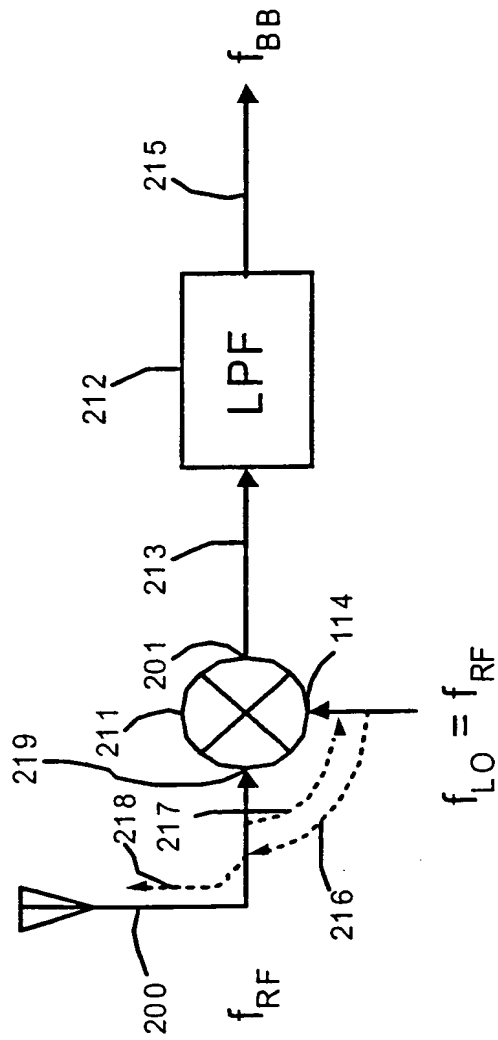
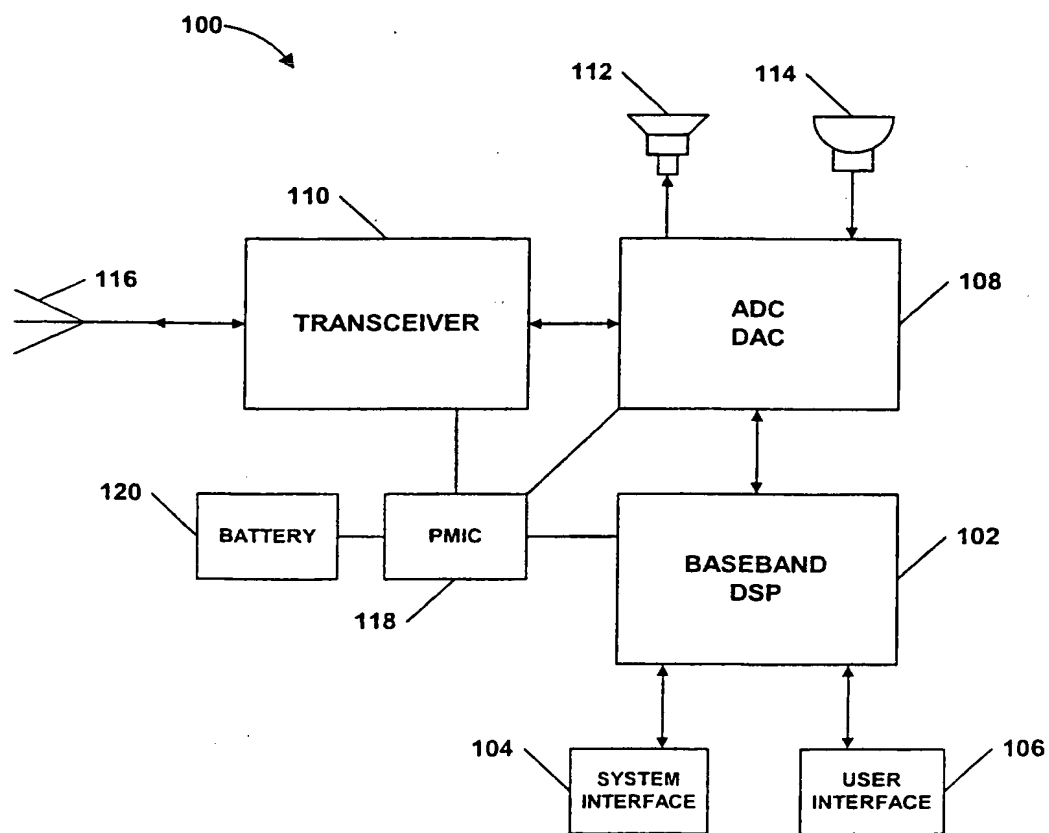


Figure 4

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**Figure 5**

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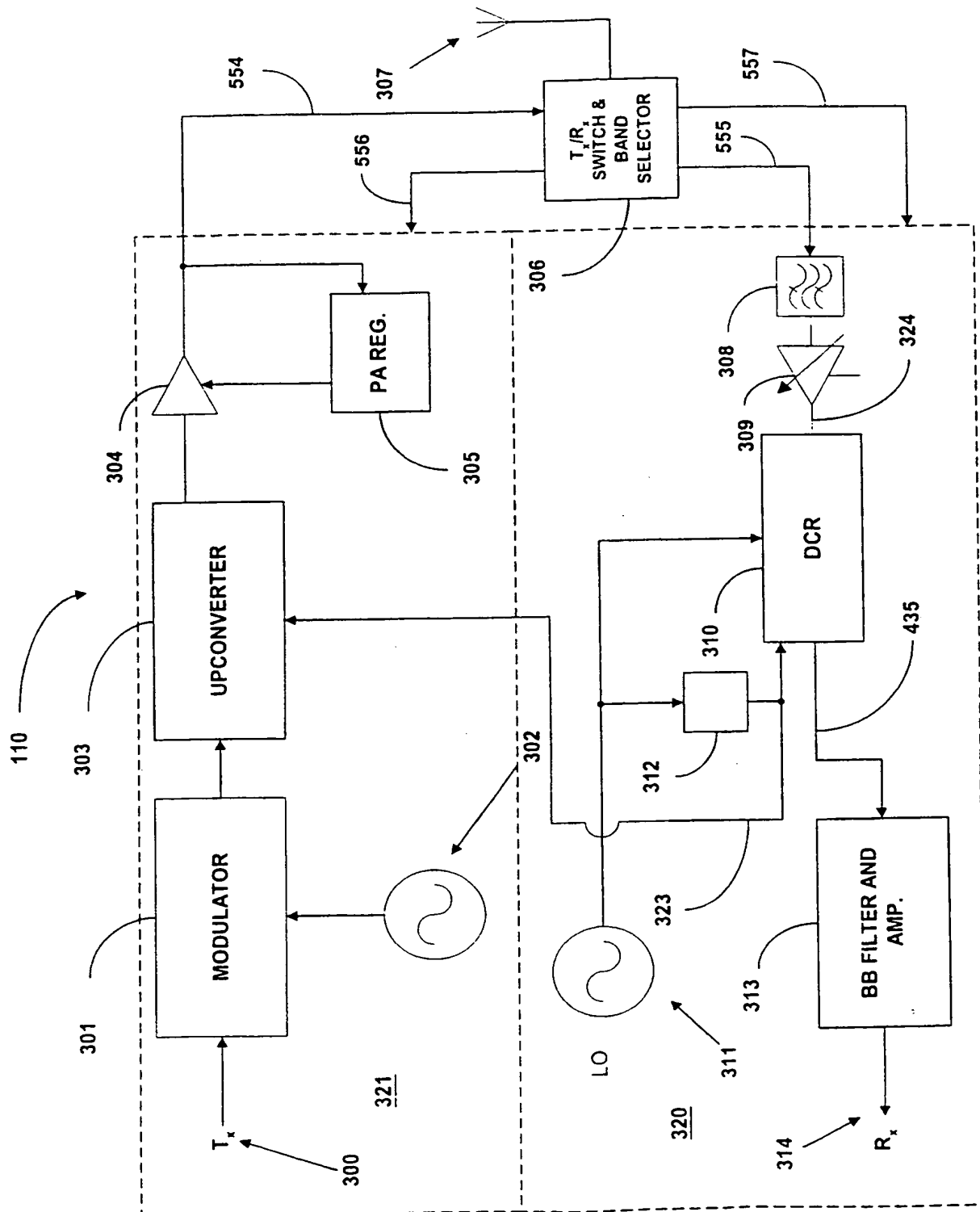


Figure 6

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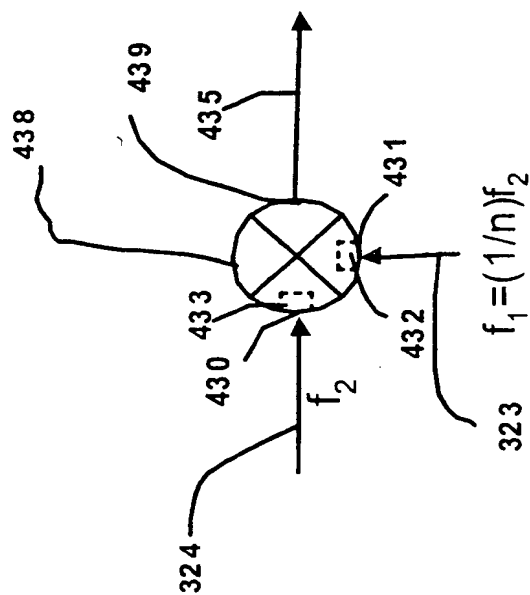


Figure 7

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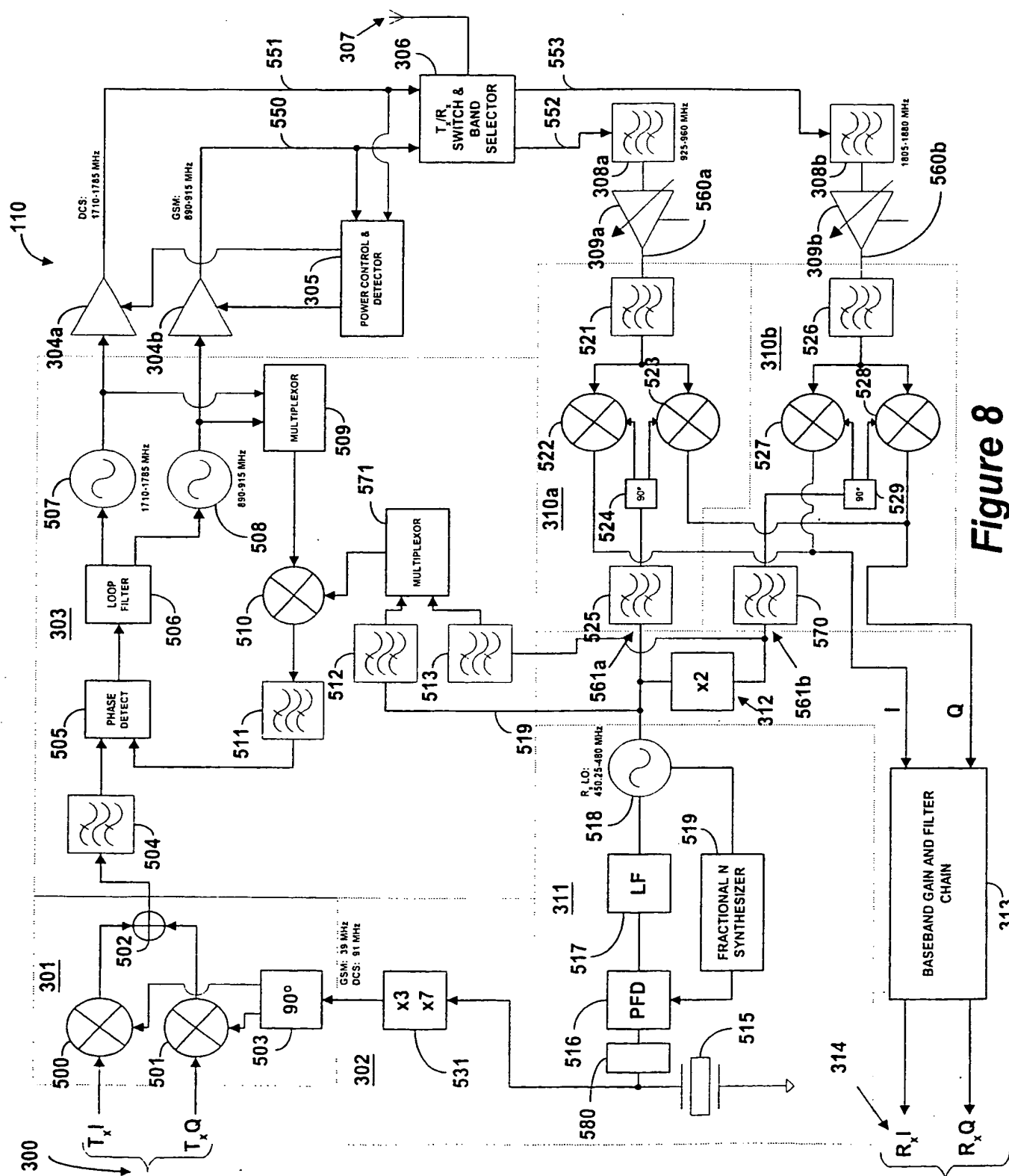


Figure 8

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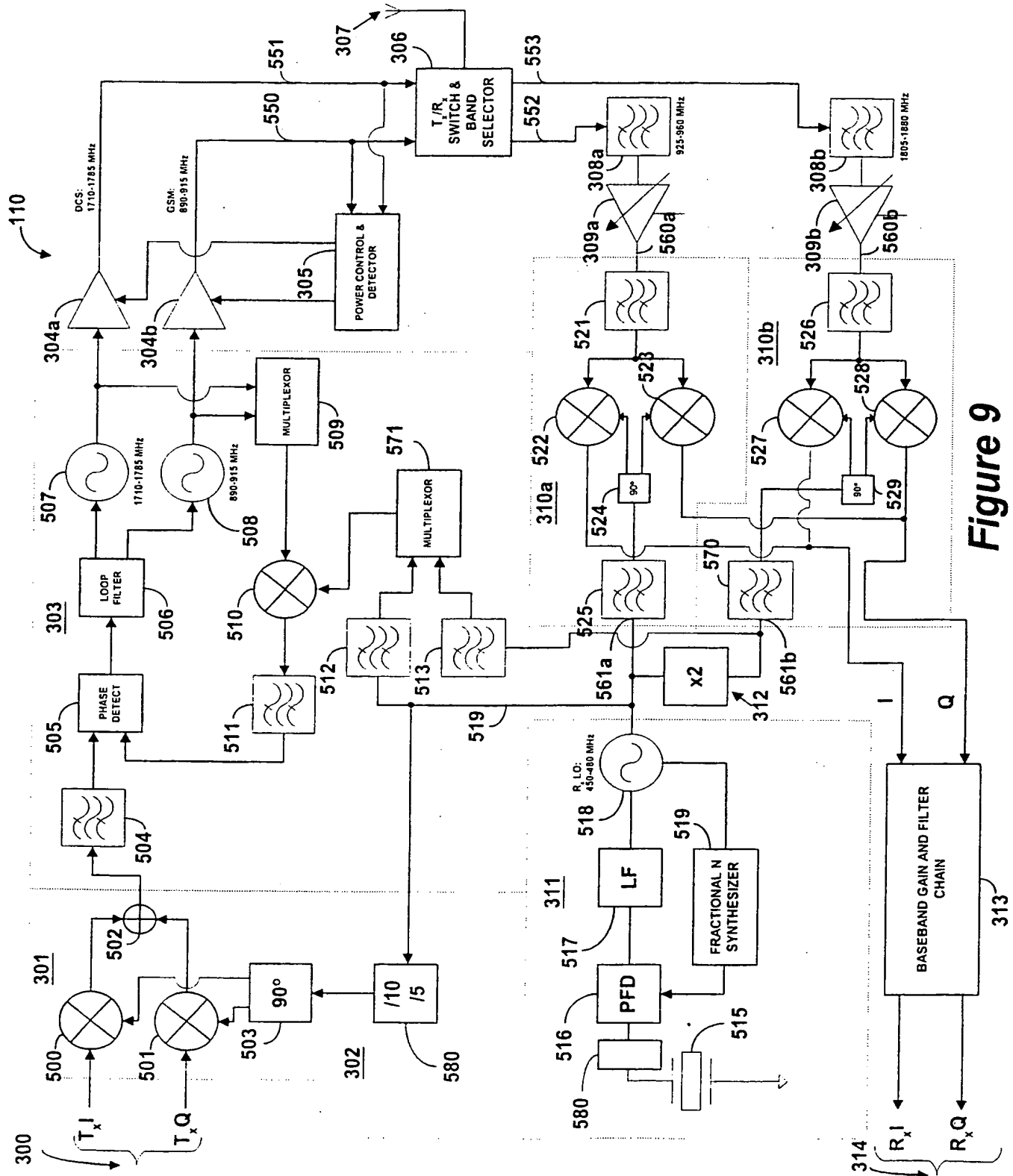


Figure 9

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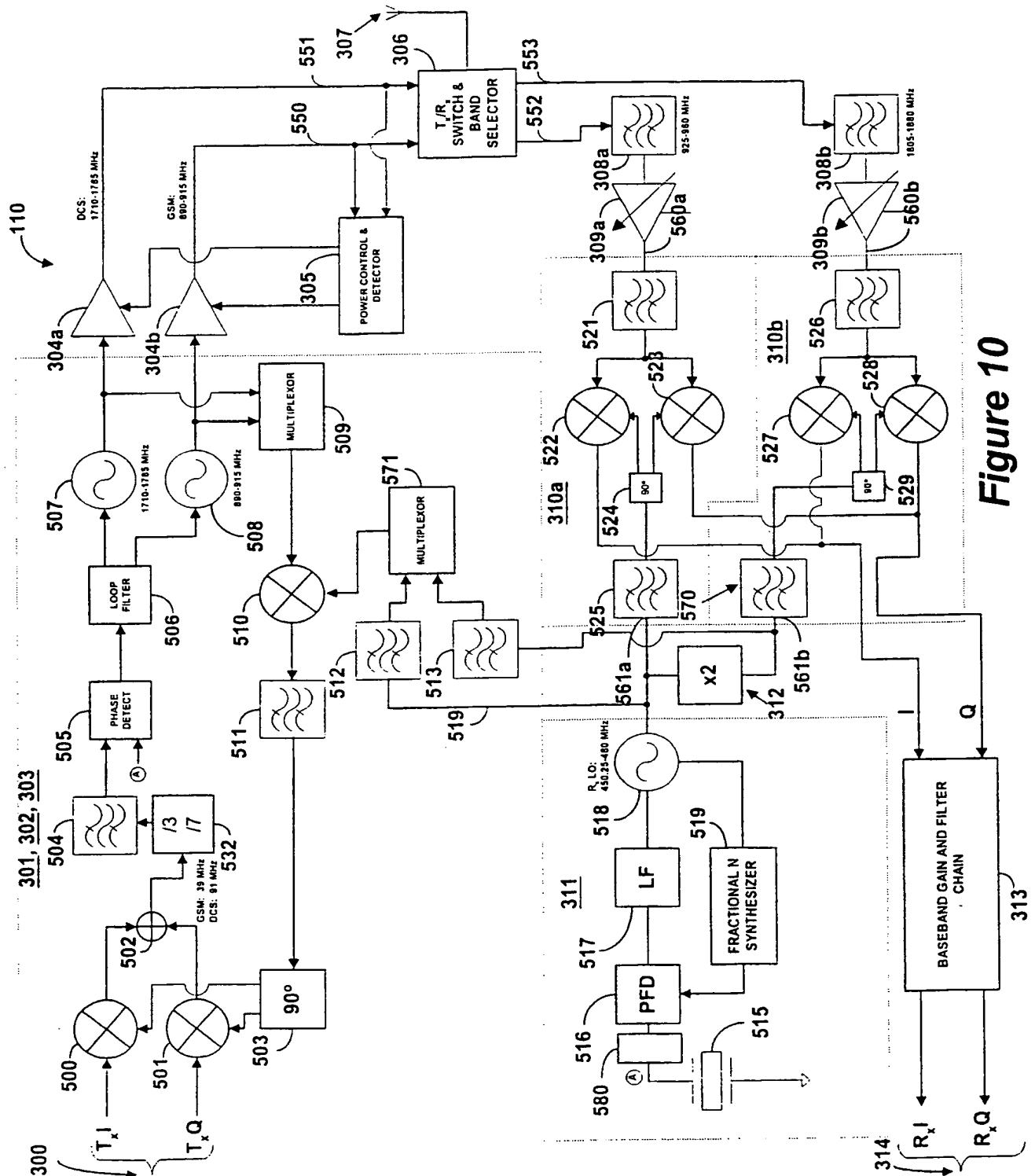
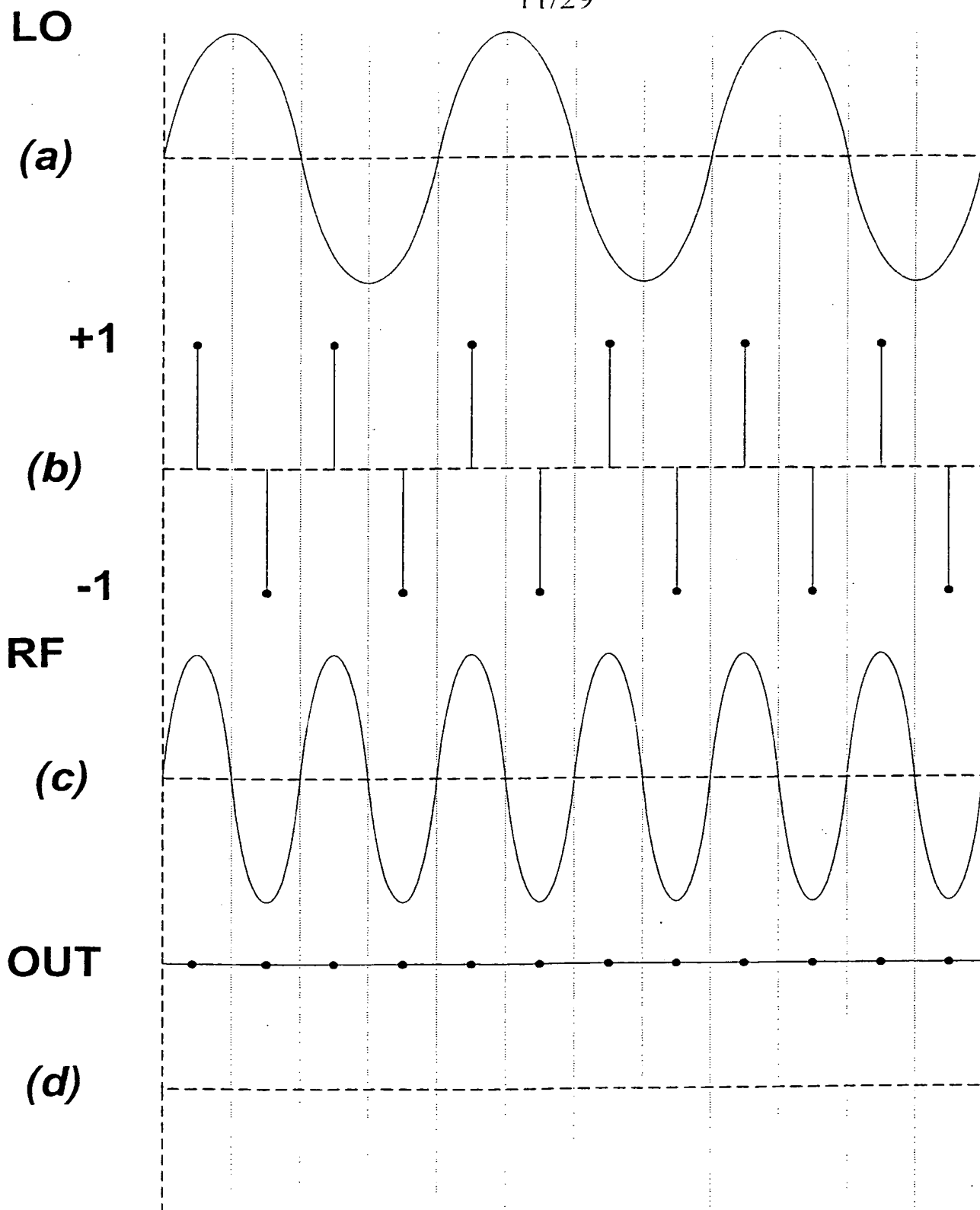


Figure 10

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**FIGURE 11**

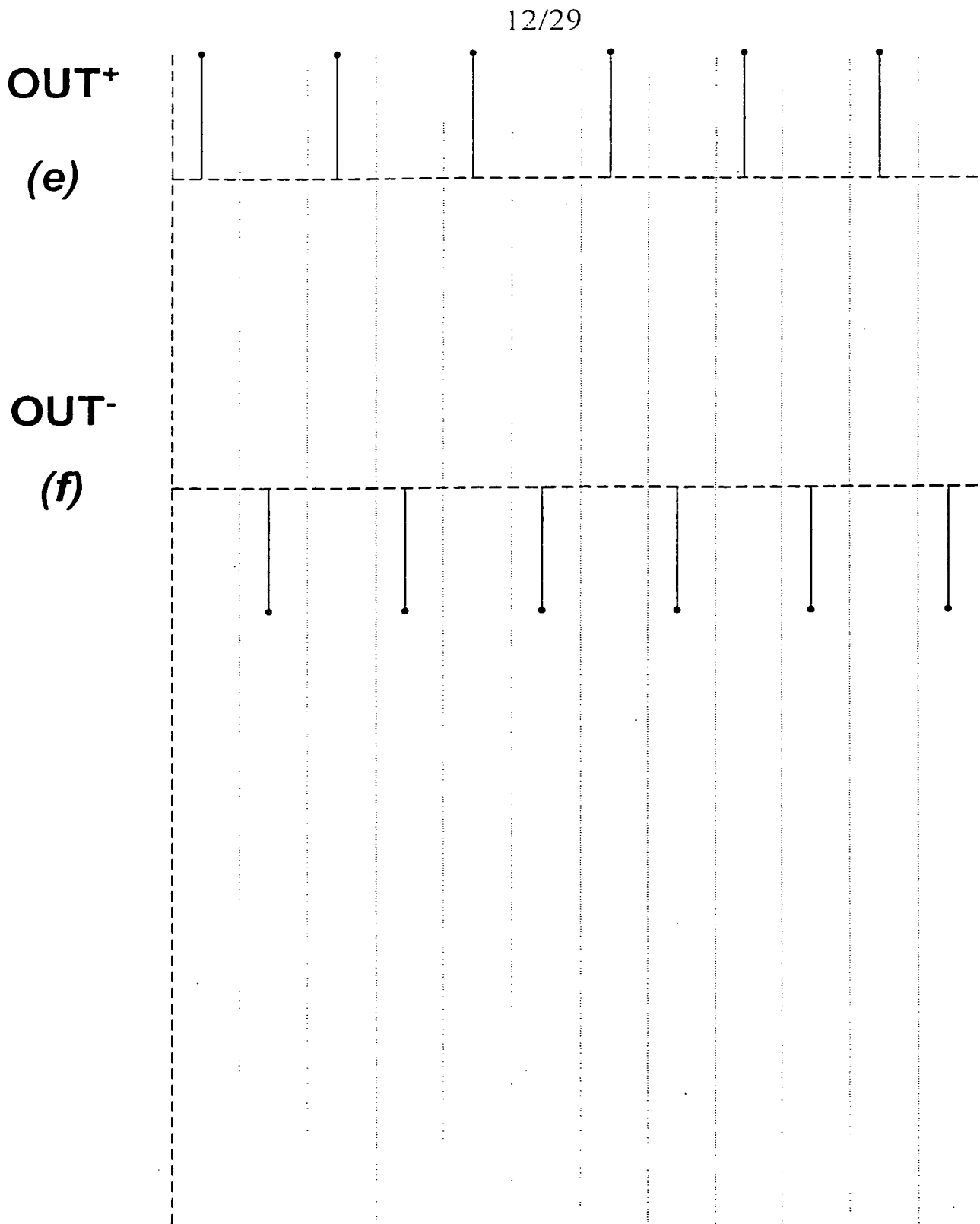
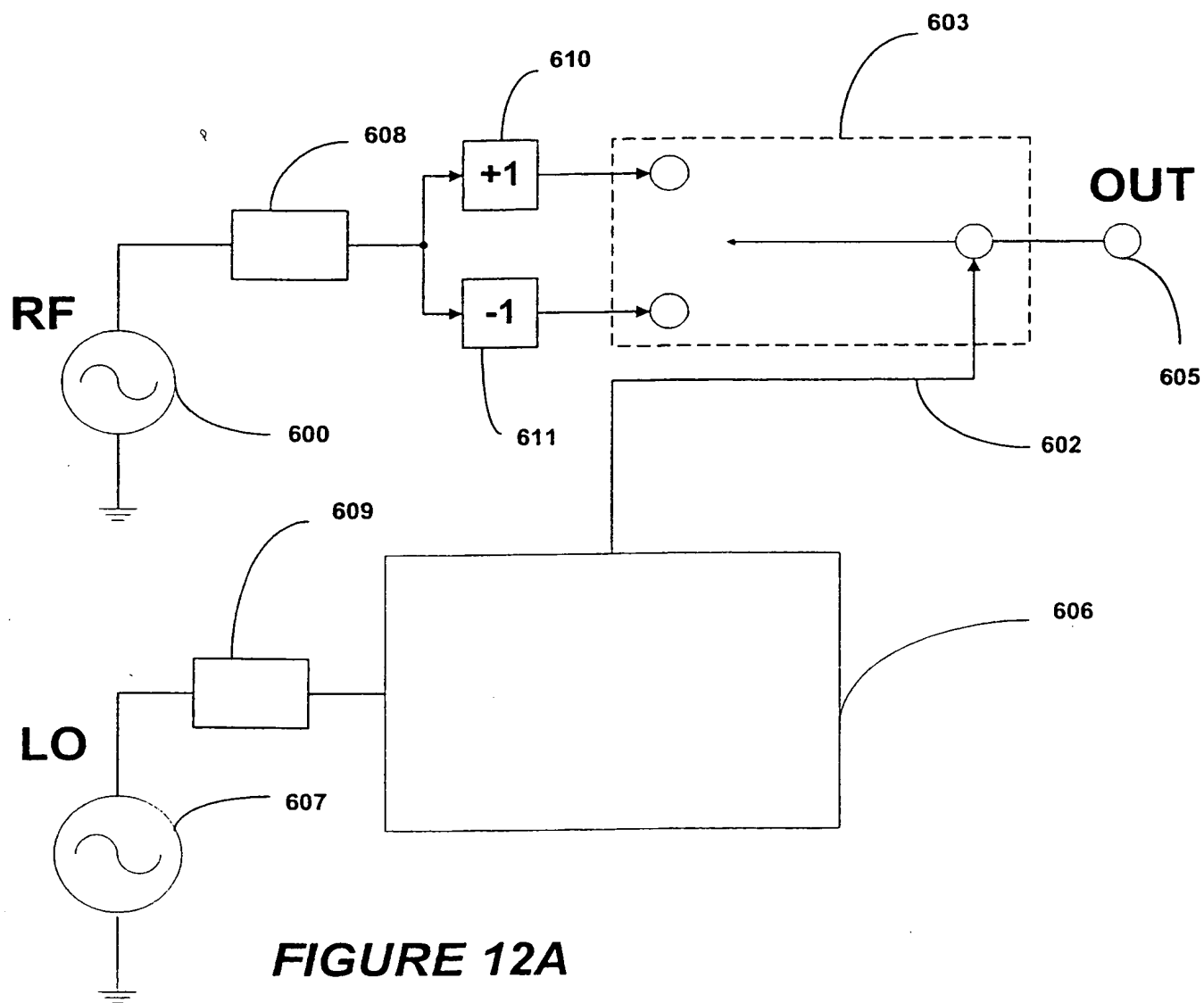
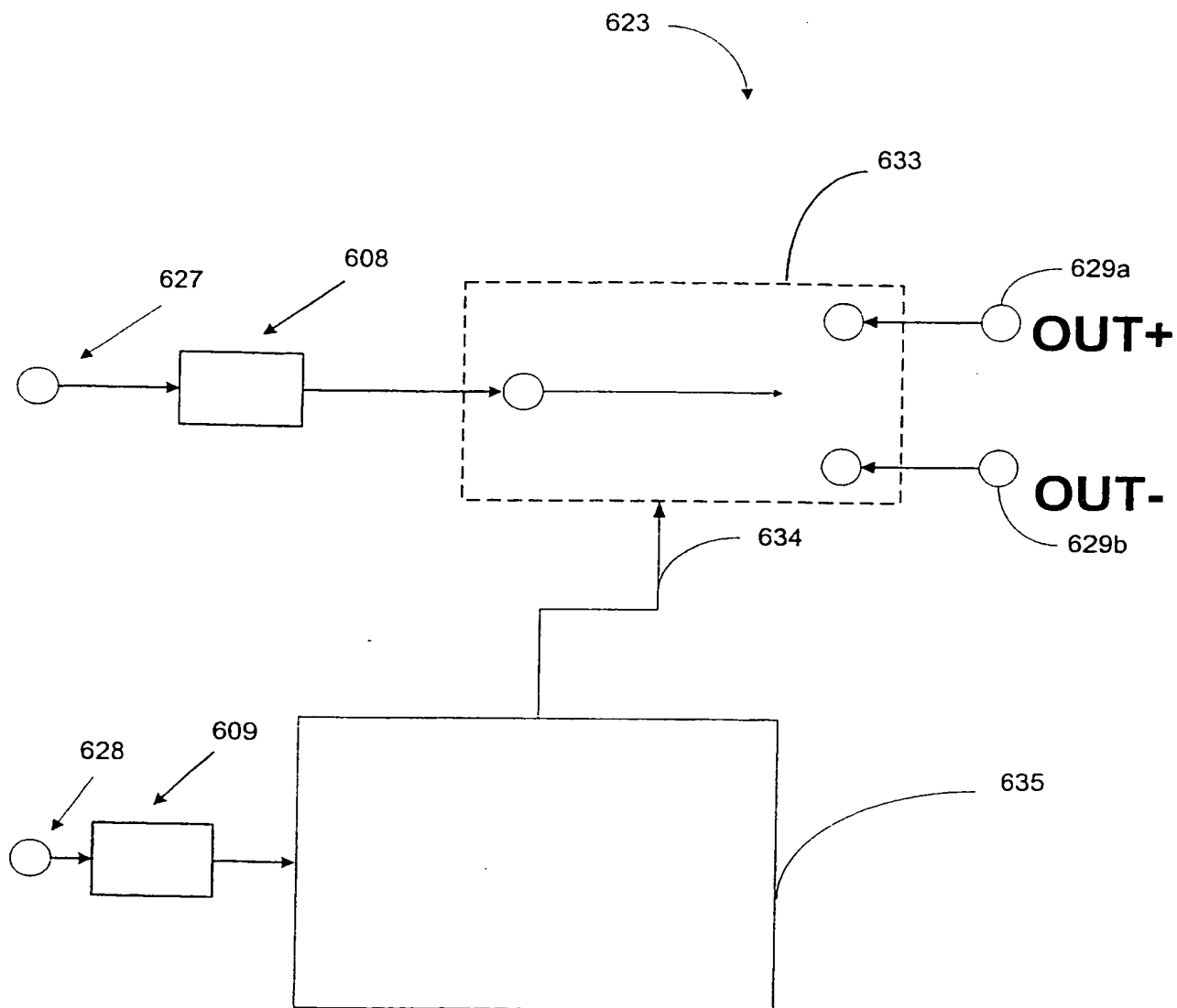


FIGURE 11
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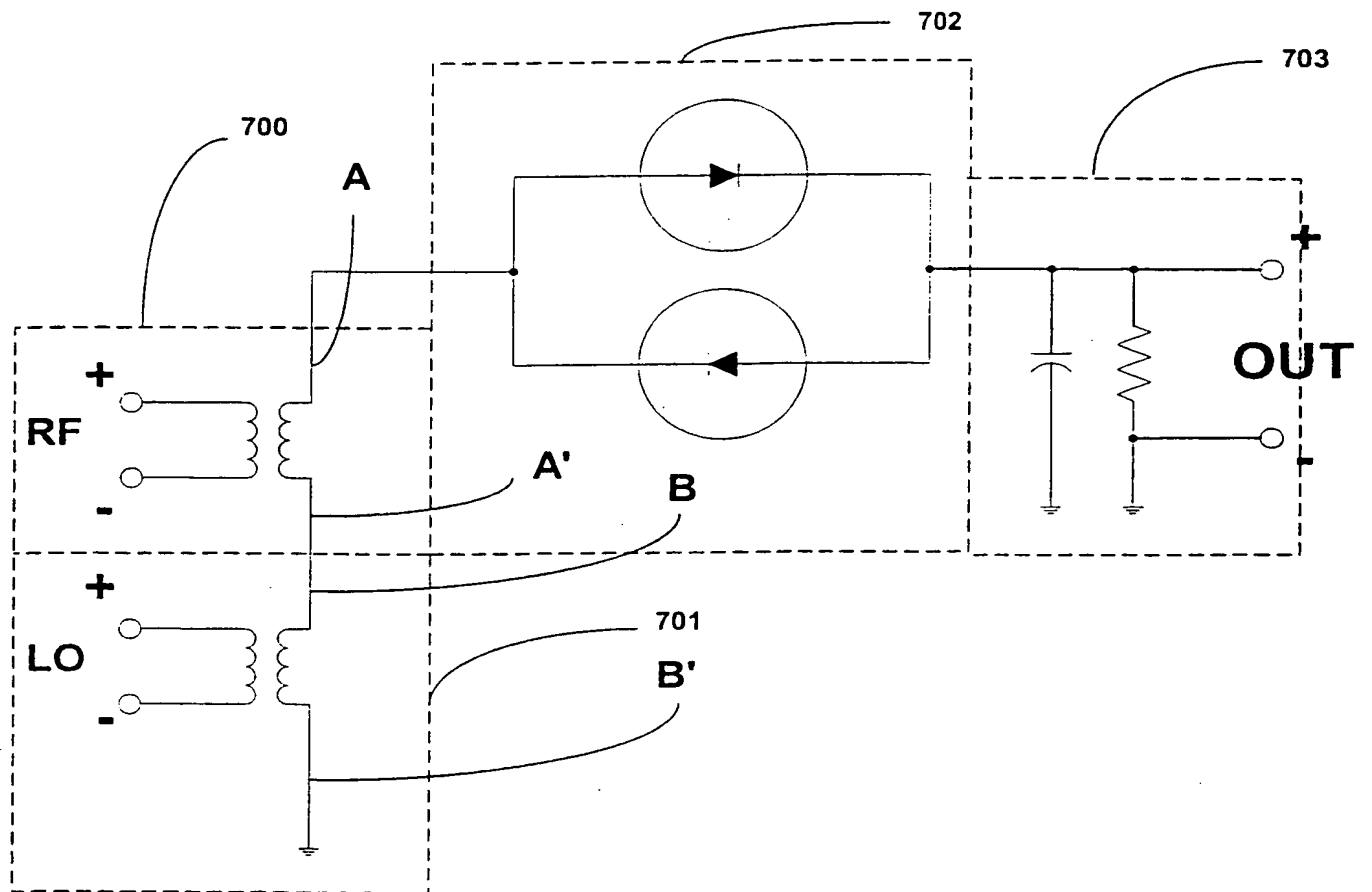
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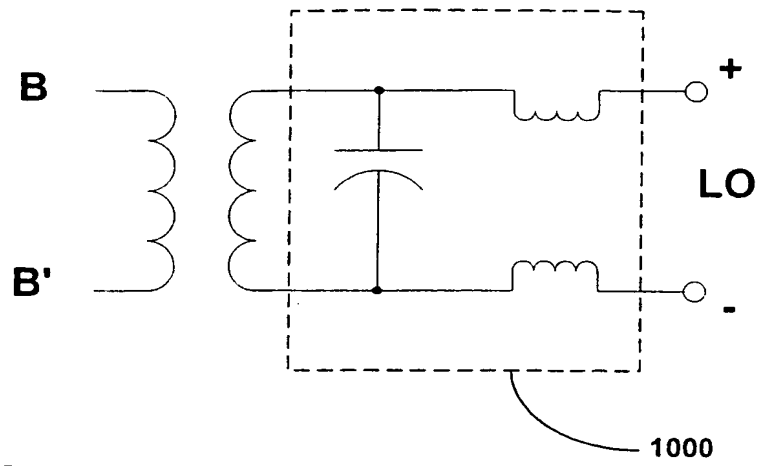
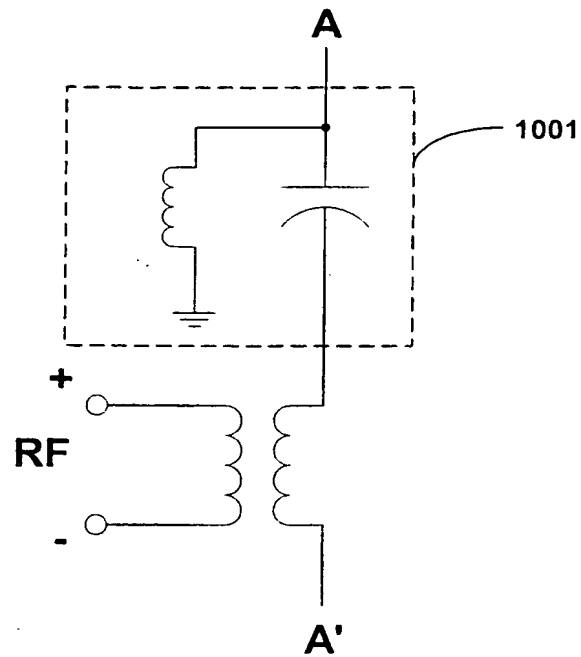
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**FIGURE 12B**

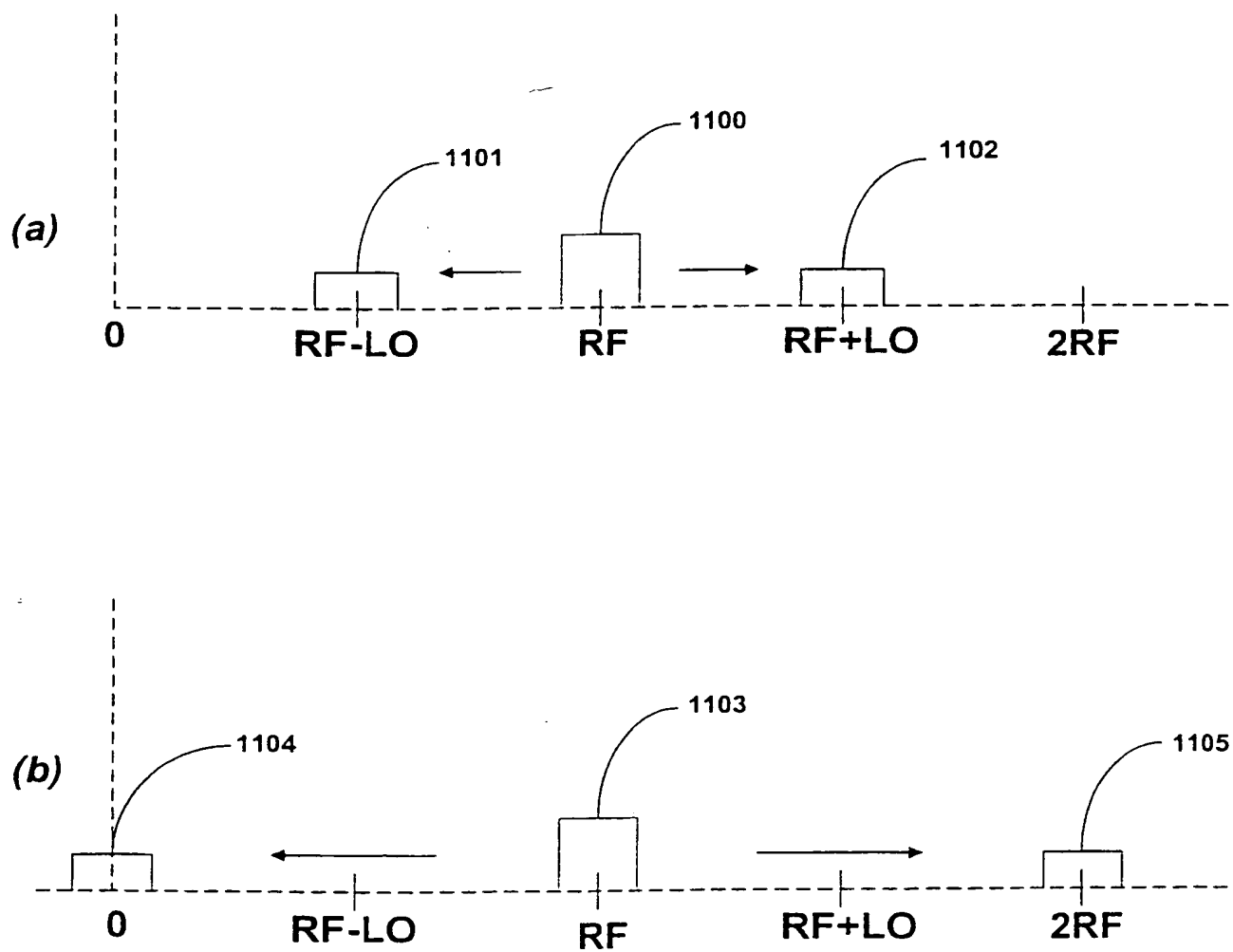
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**FIGURE 13**

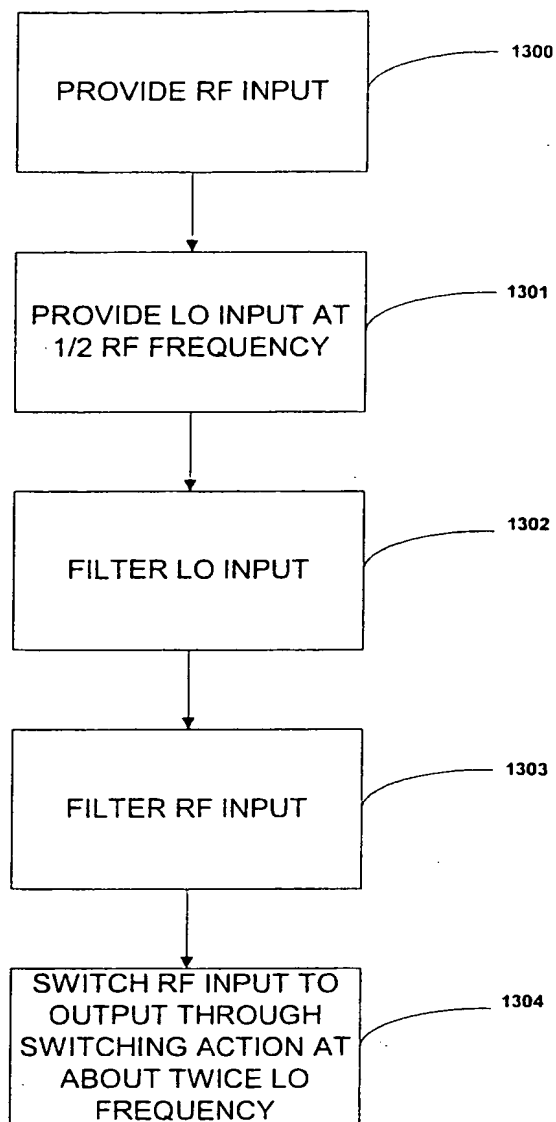
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**FIGURE 14A****FIGURE 14B**

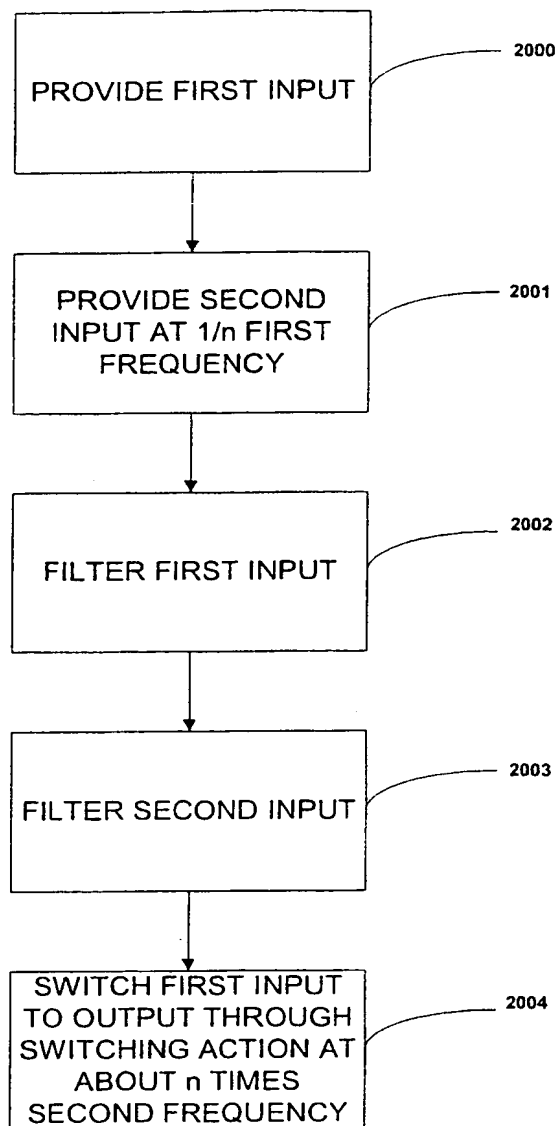
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**FIGURE 15**

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**FIGURE 16**

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**FIGURE 17**

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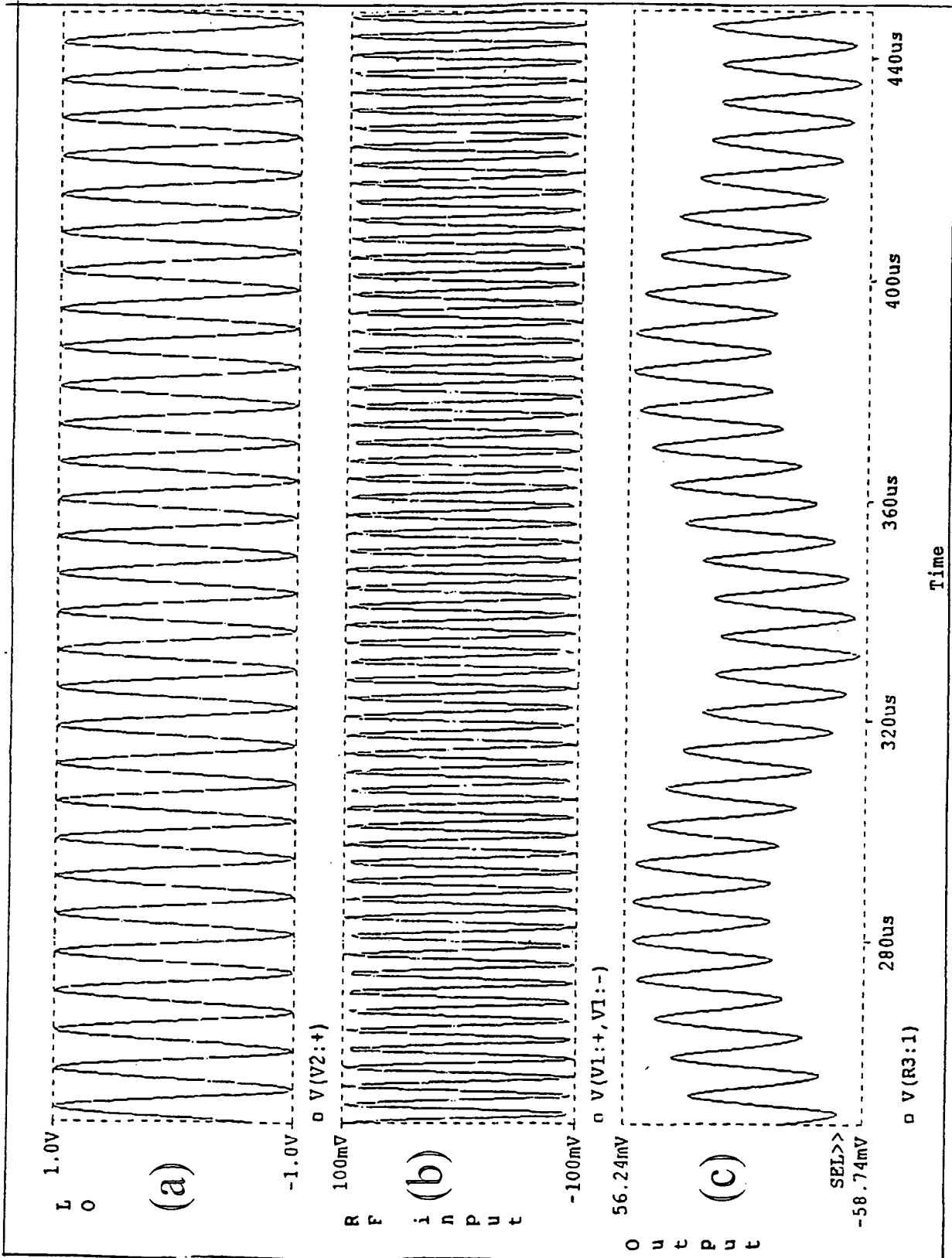


Figure 18

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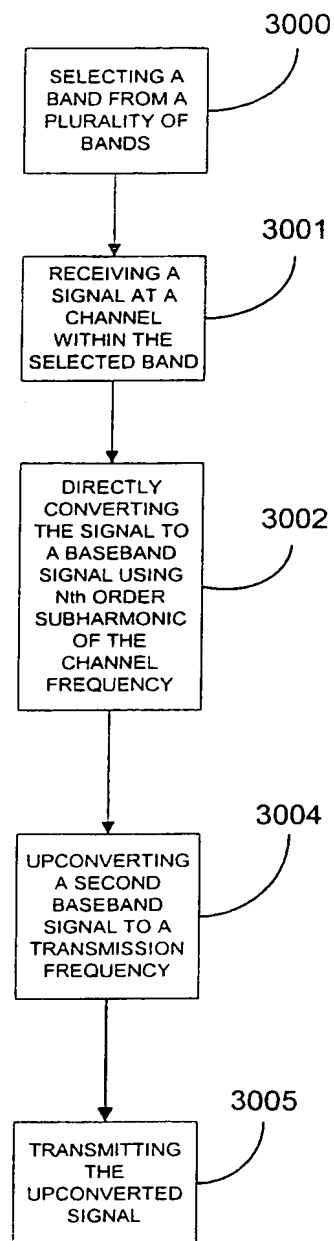


FIGURE 19

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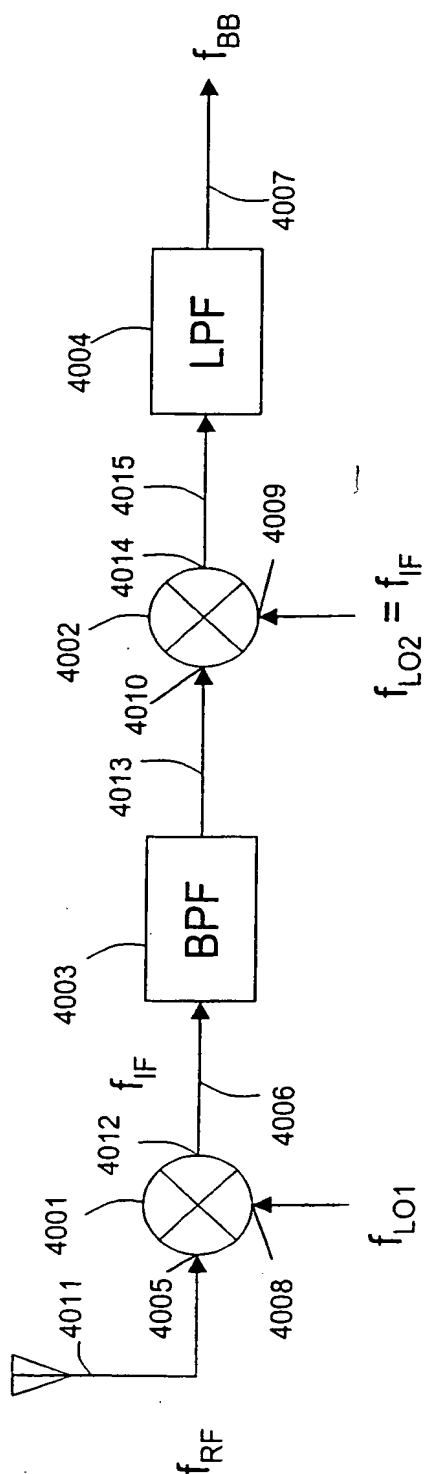


Figure 20

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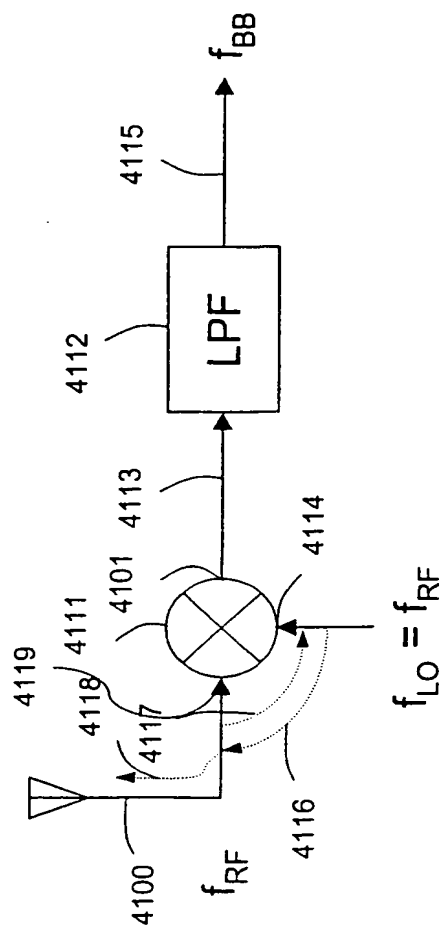


Figure 21

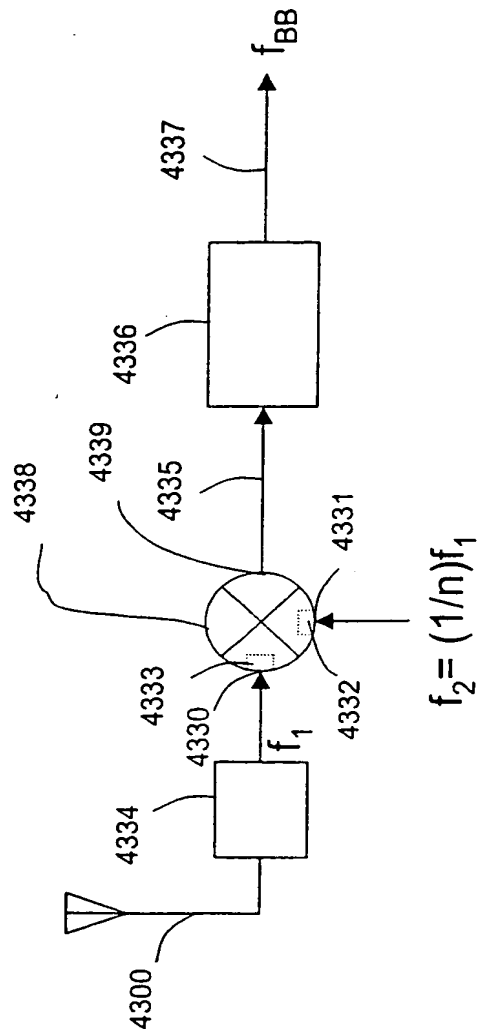


Figure 22

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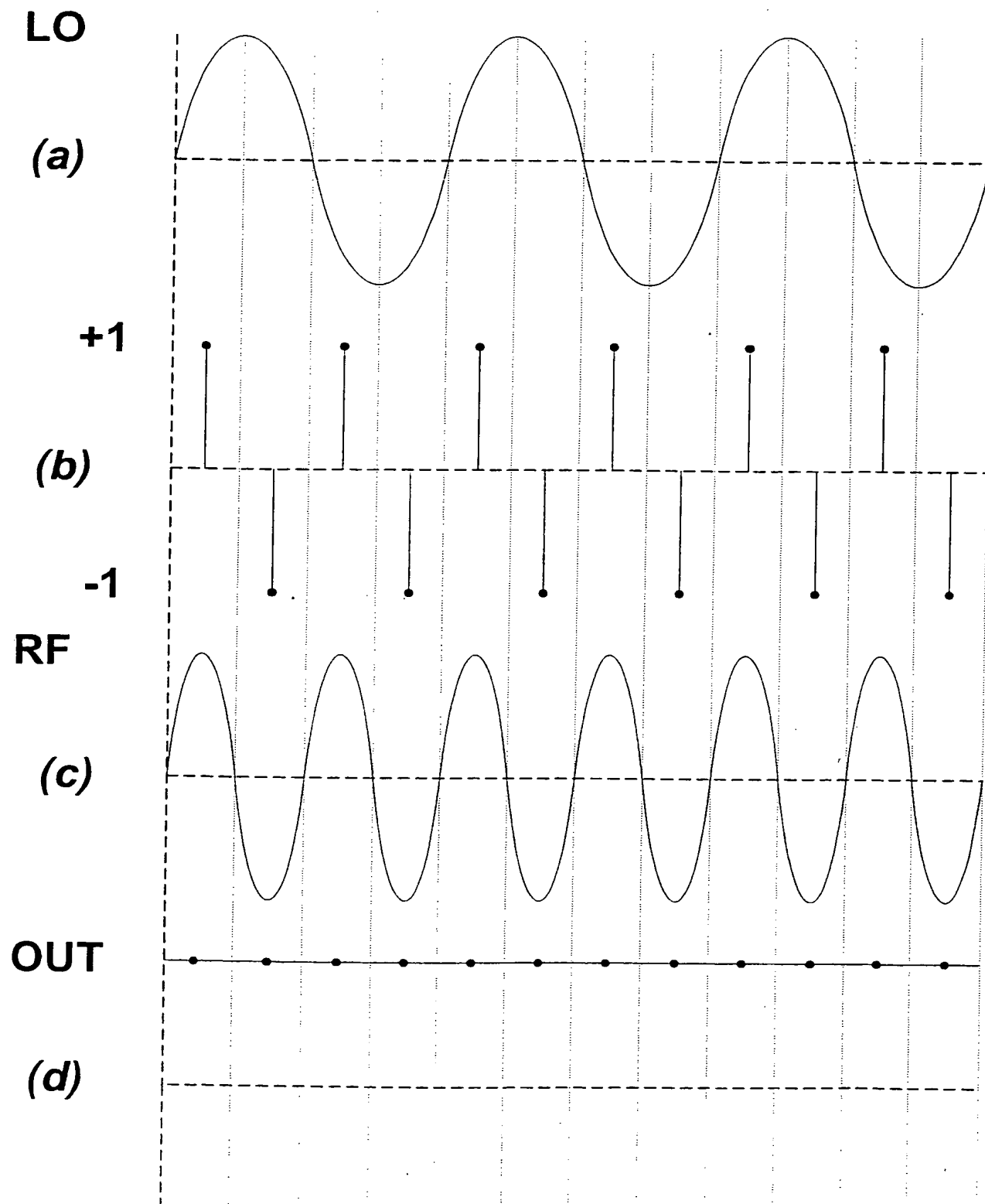
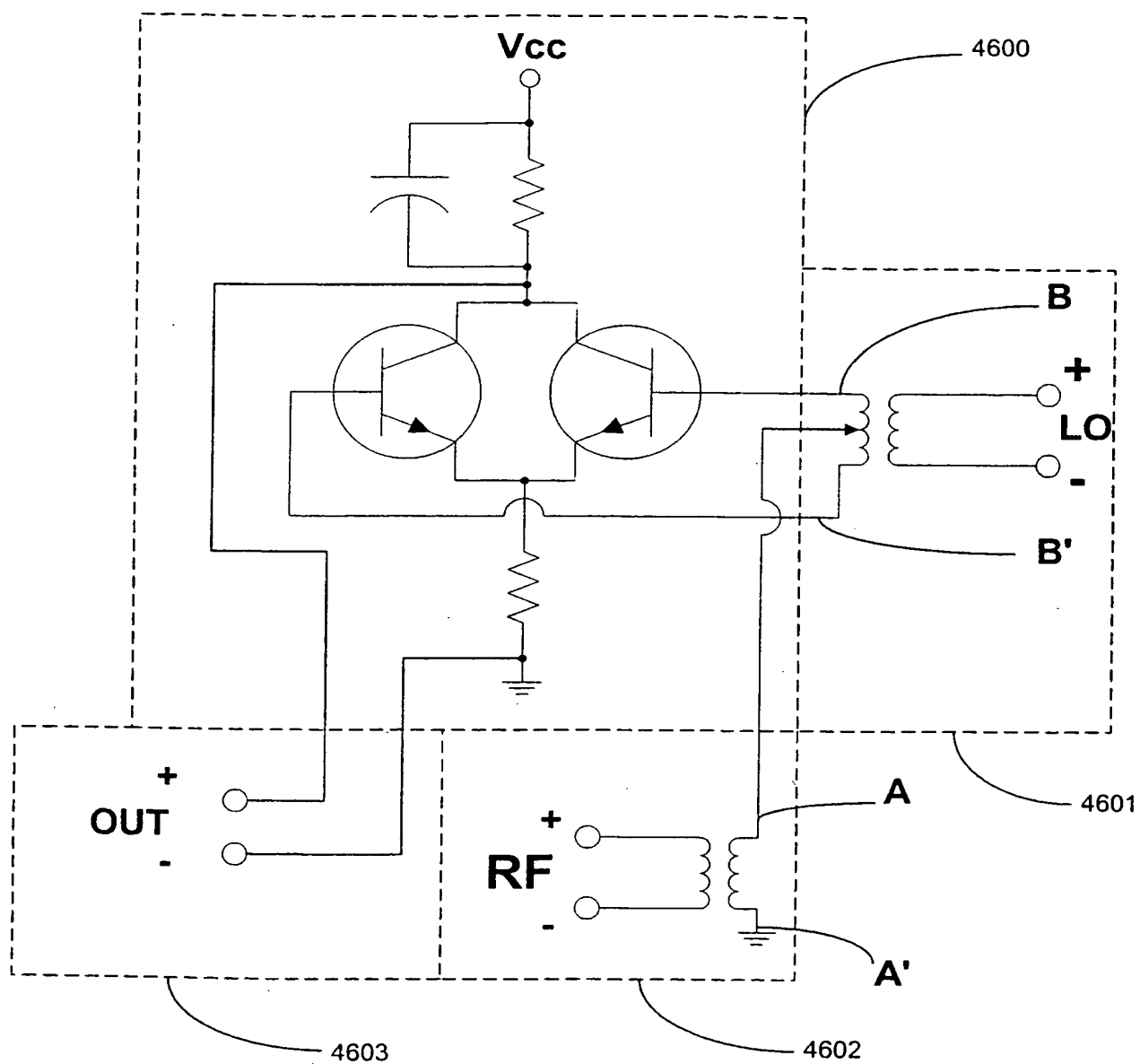
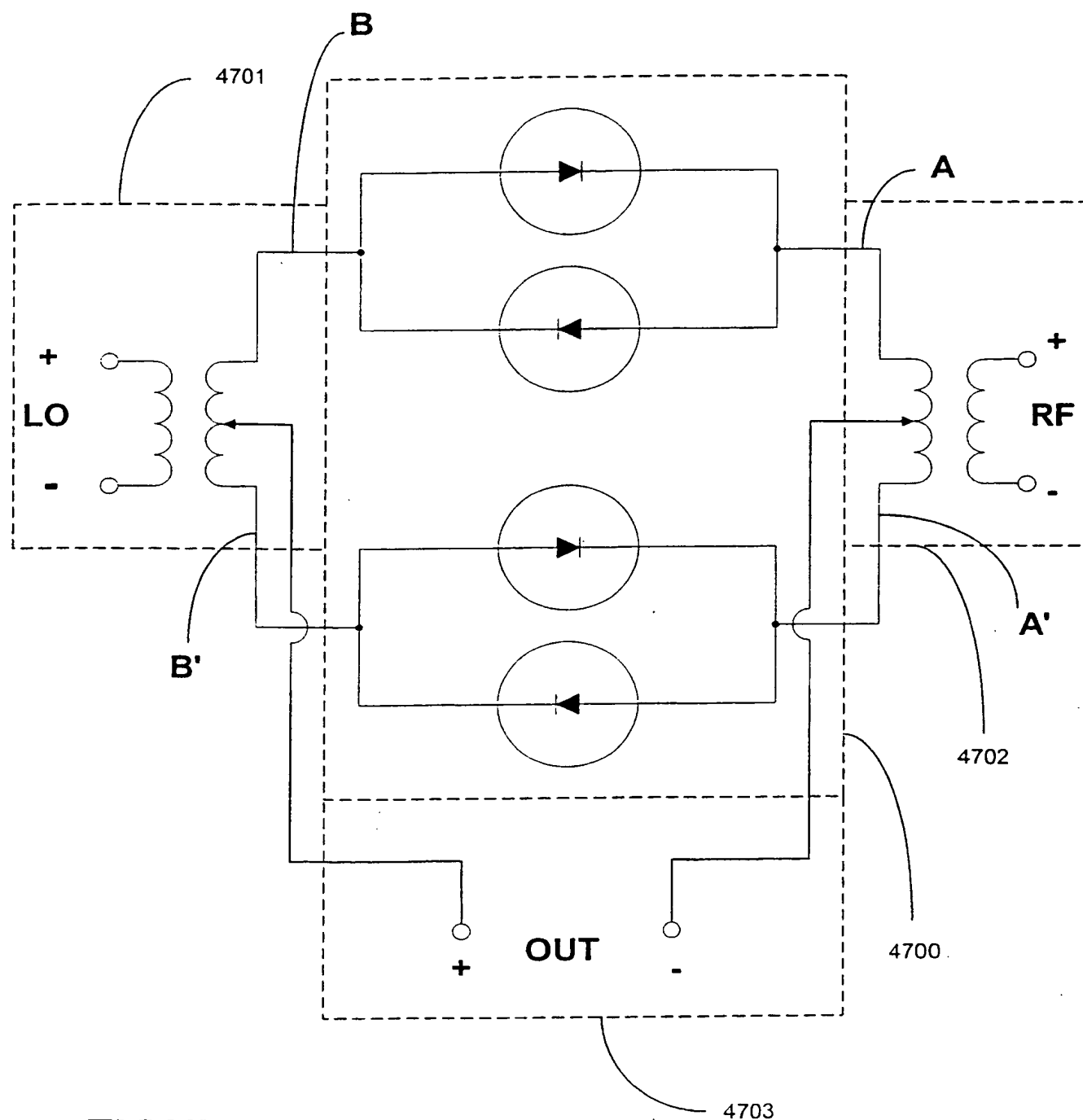


Figure 23

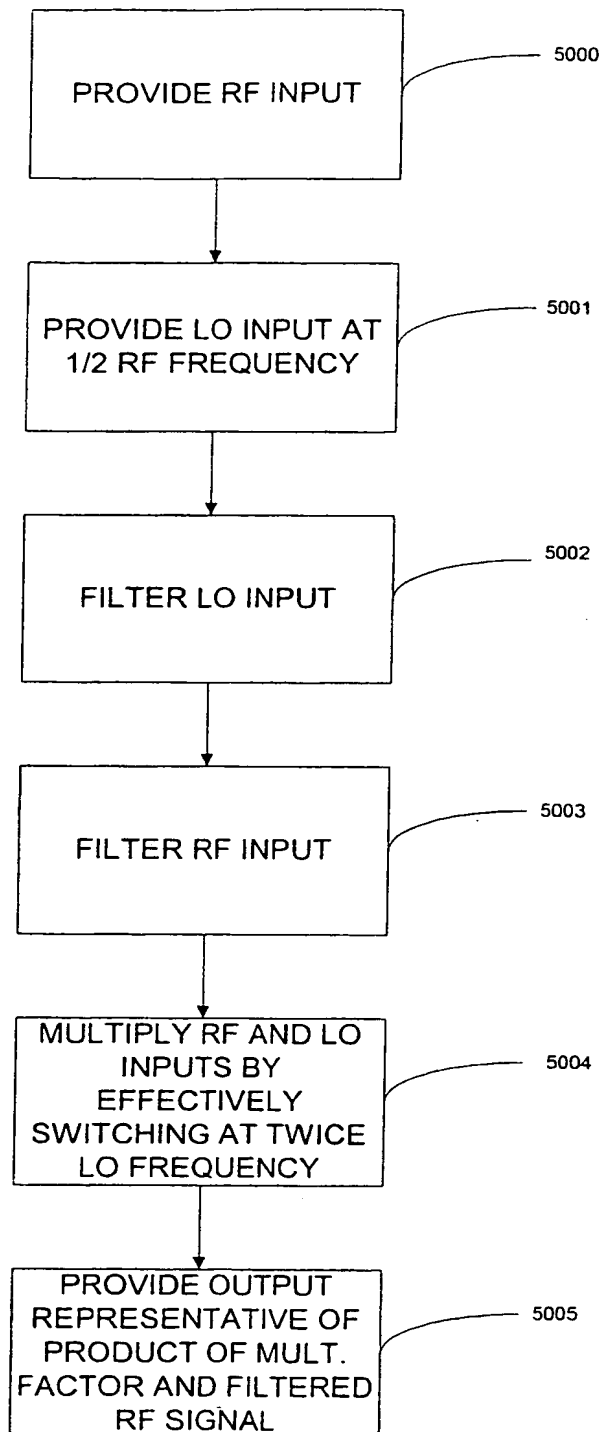
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**FIGURE 24**

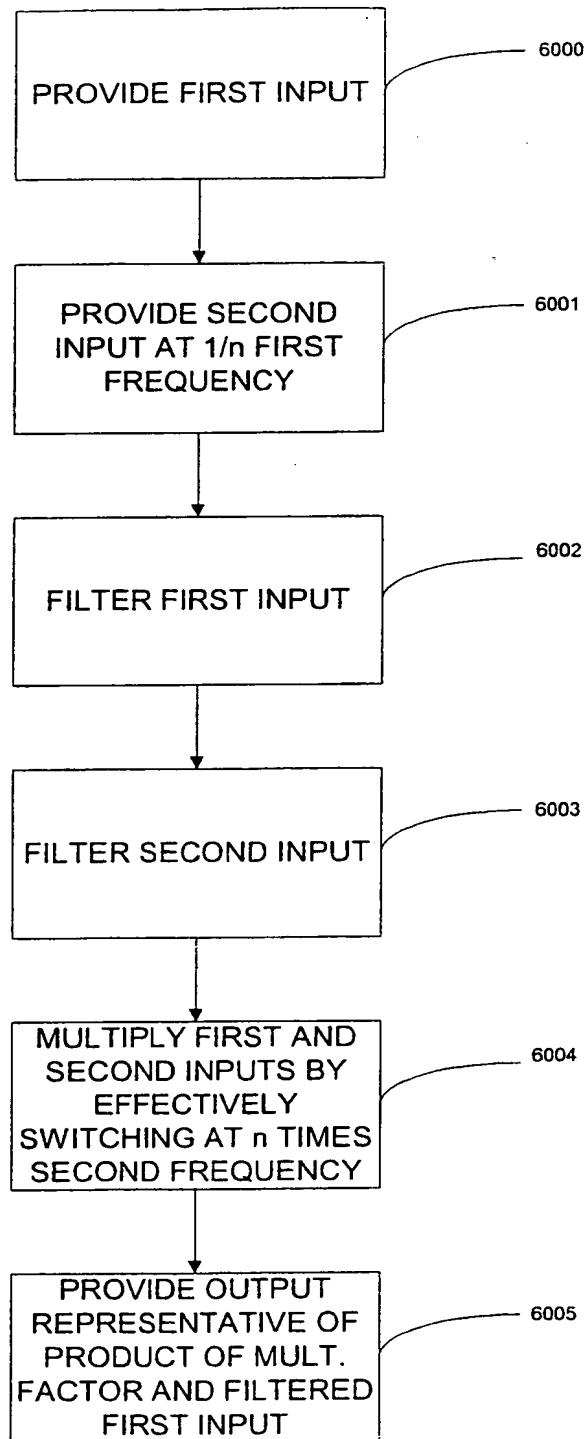
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**FIGURE 25**

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**FIGURE 26**

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**FIGURE 27**

INTERNATIONAL SEARCH REPORT

International Application No

PCT/US 00/05379

A. CLASSIFICATION OF SUBJECT MATTER
IPC 7 H04B1/40 H04B1/30

According to International Patent Classification (IPC) or to both national classification and IPC

B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)
IPC 7 H04B

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, WPI Data

C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X A	EP 0 798 880 A (NOKIA MOBILE PHONES LTD) 1 October 1997 (1997-10-01) abstract page 4, line 40 -page 10, line 16 claims 1-3 figures 2,3,5 ---	1, 11, 12, 14-17 2-10, 13, 18-49
X A	WO 92 01337 A (C COM GROUP PLC) 23 January 1992 (1992-01-23) abstract page 4, line 14 - line 34 claims 1-3 figure 1 -----	32-36, 42-46, 48 1-31, 37-41, 47, 49

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Patent document cited in search report	Publication date	Patent family member(s)	Publication date
EP 0798880 A	01-10-1997	FI 961428 A US 5983081 A	30-09-1997 09-11-1999
WO 9201337 A	23-01-1992	NONE	

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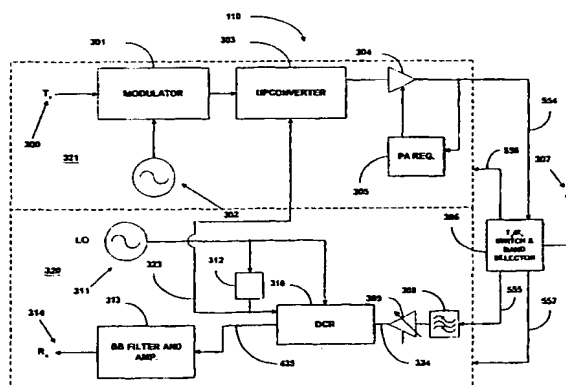
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- (74) Agent: LAURENSEN, Robert, C.; Howrey Simon Arnold & White LLP, 1299 Pennsylvania Avenue, N.W., Box 34, Washington, DC 20004-2402 (US).
- (81) Designated States (*national*): CN, KR.
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- For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.

(54) Title: MULTI-BAND TRANSCEIVER UTILIZING DIRECT CONVERSION RECEIVER AND DIRECT CONVERSION RECEIVER



(57) Abstract: A multi-band transceiver having a receiver portion and a transmitter portion, wherein the receiver portion includes a direction conversion system for directly downconverting a signal to baseband frequencies. The direct conversion receiver system includes a frequency translator having first and second inputs and an output. The first frequency is preferably an n th order subharmonic of the second frequency, wherein n is an integer greater than 1. A low pass filter is integral with or inherent to the first input and a high pass filter is integral with or inherent to the second input. A direct conversion receiver system is also provided in which a first input signal at a first frequency is applied to a first input port of a multiplier, and a second input signal at a second frequency equal to about $1/n$ times the first frequency wherein n is an integer, is applied to a second input port of the multiplier. A first filter coupled to the first input port is configured to substantially filter out any leakage at the second frequency which may be present. A second filter is configured to substantially filter out any leakage at the first frequency which may be present. The multiplier is configured to produce a signal at an output port thereof which is derived from the product of the first and second signals.

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